



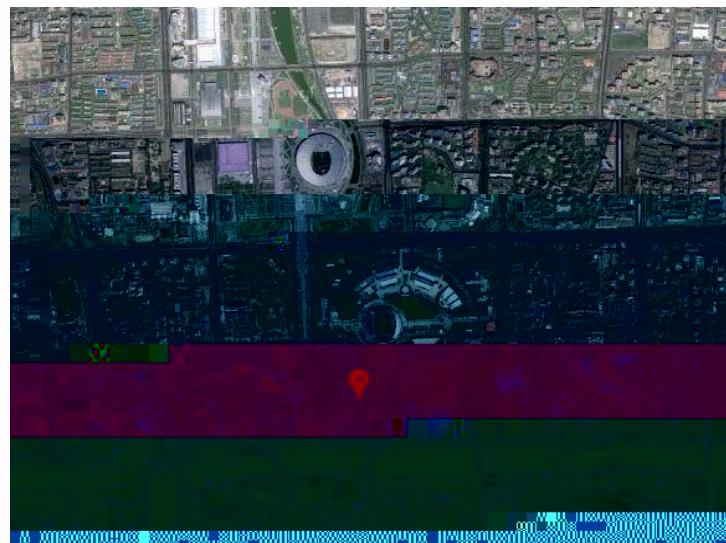
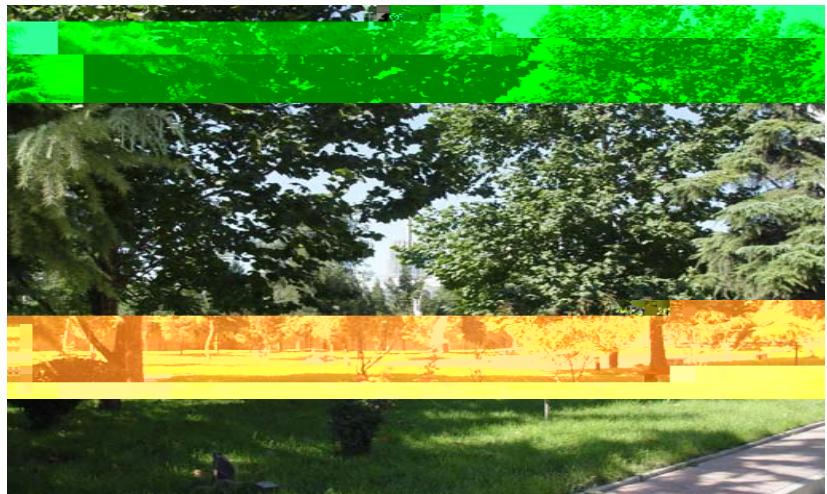
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EDA

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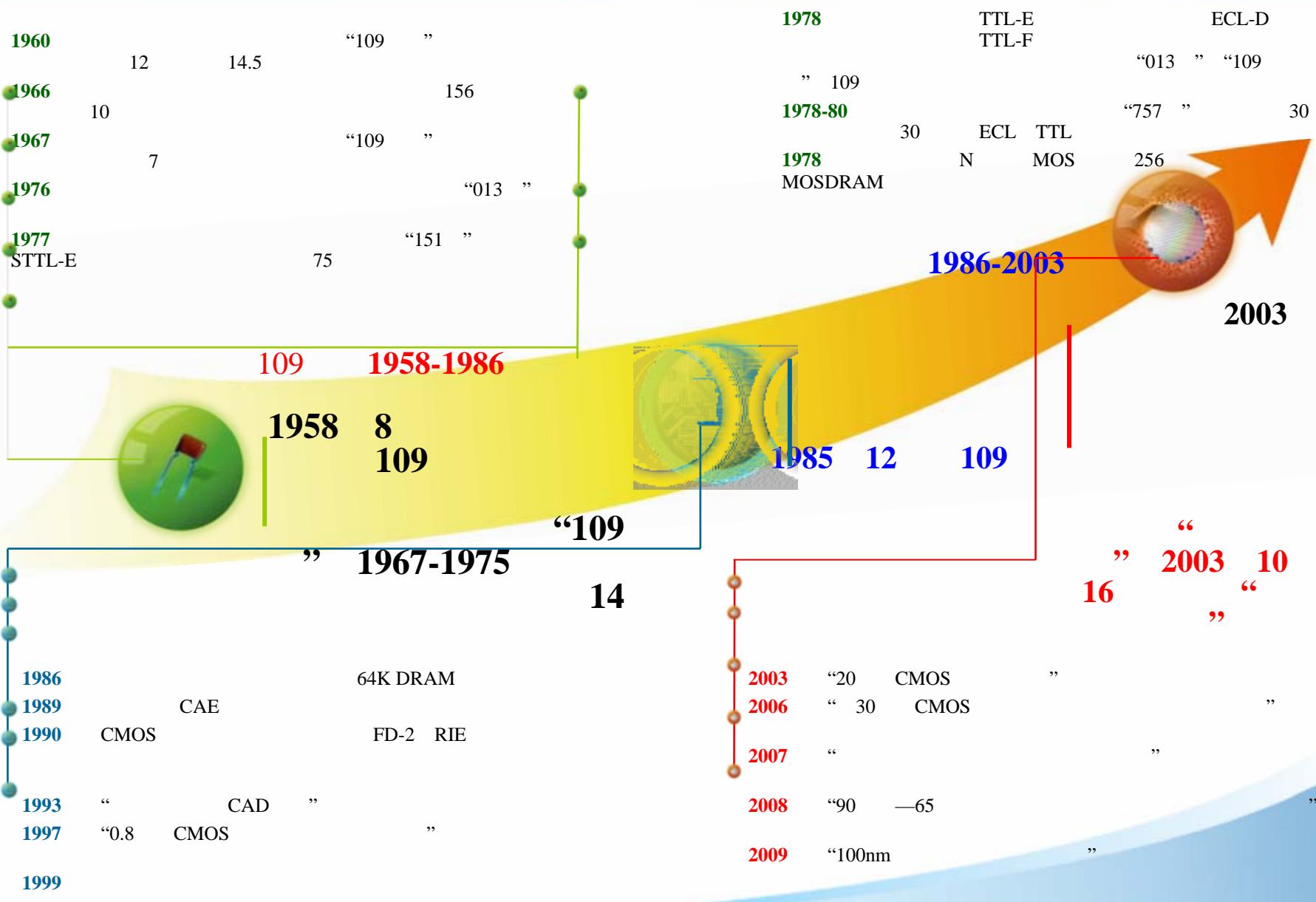
300

EDA

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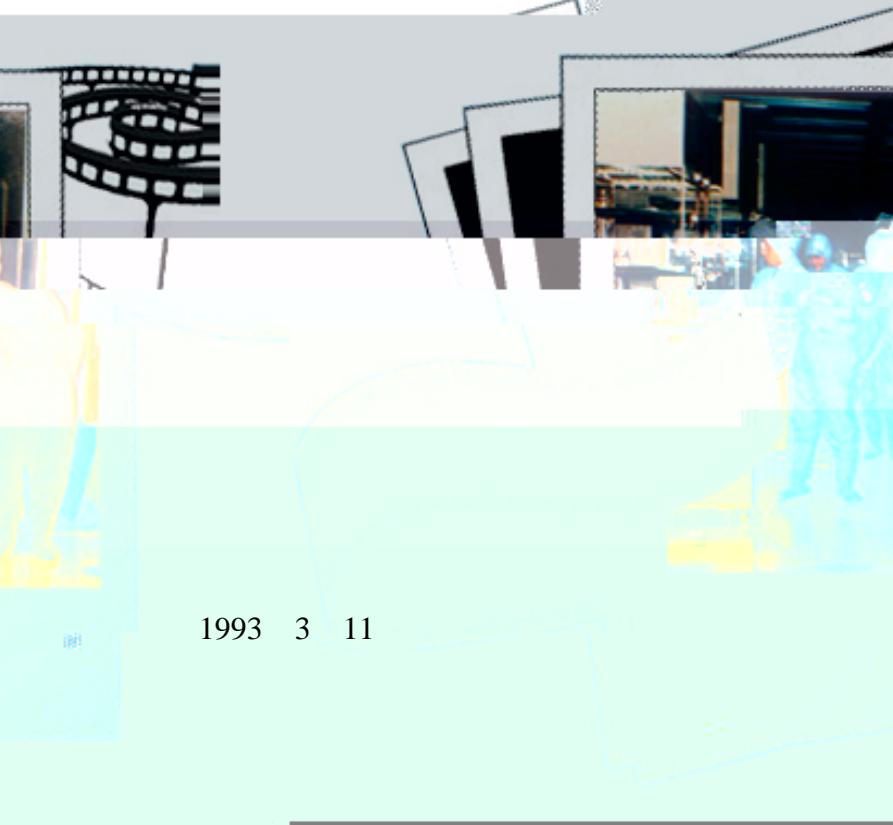




109



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1985 5 25



1986  
109

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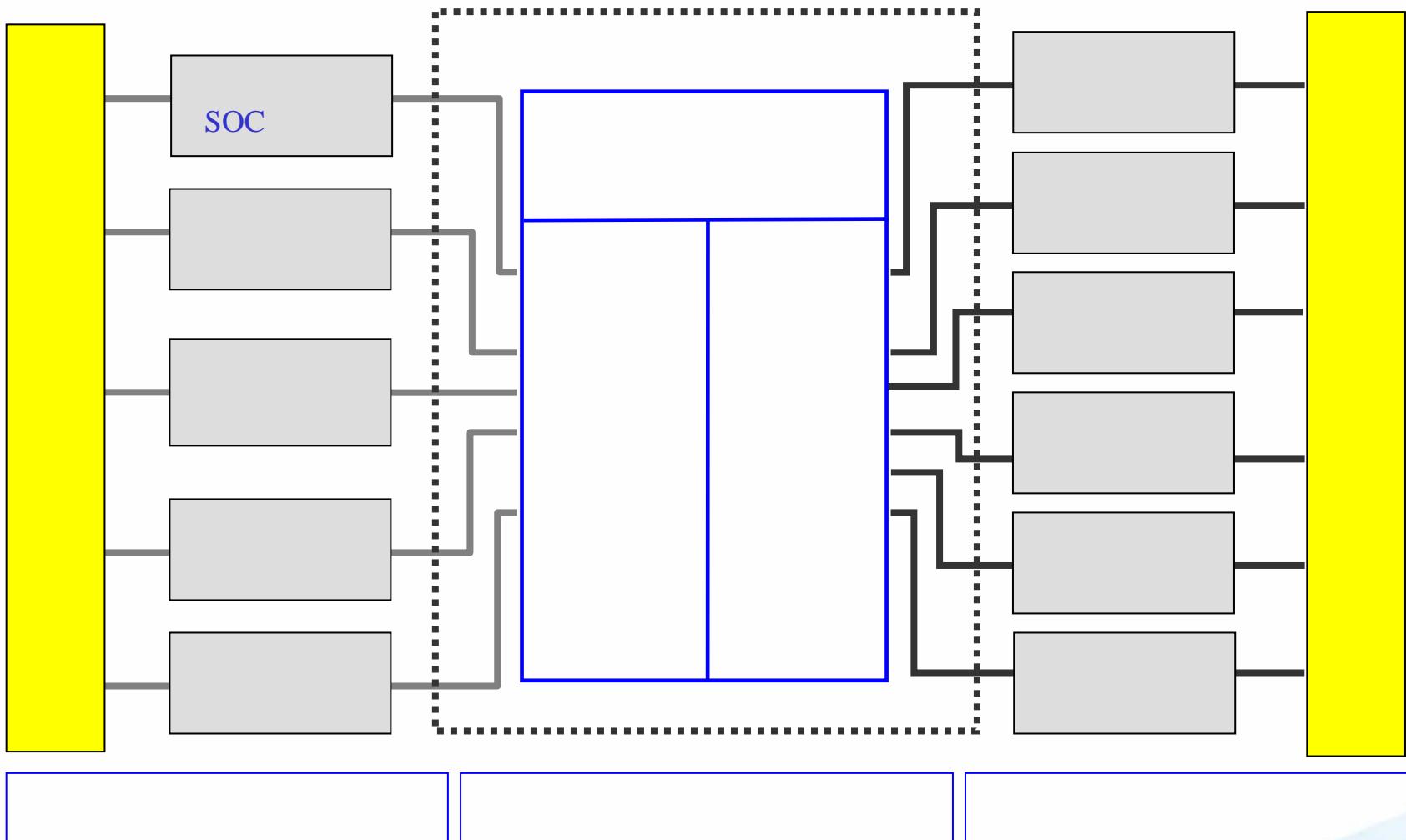
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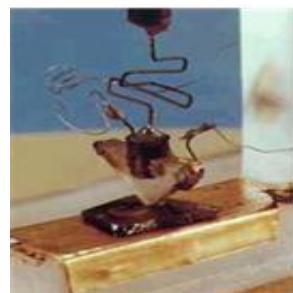
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# Top-down



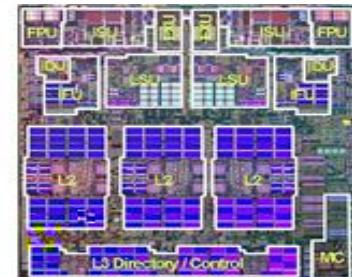
First Transistor  
By  
J. Bardeen and  
W. Brattain

First Monolithic  
IC by R.N.Noyce

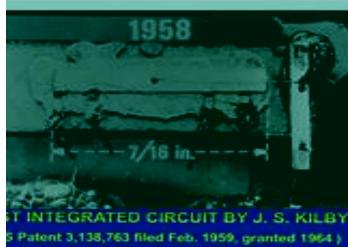
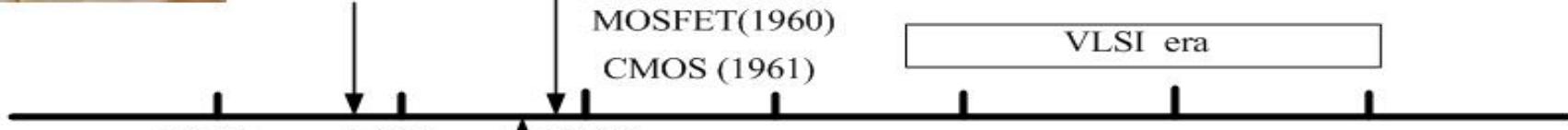


FIRST MONOLITHIC IC BY R. N. NOYCE  
(US Patent 2,981,877 filed July 1959, granted 1961)

MOSFET(1960)  
CMOS (1961)

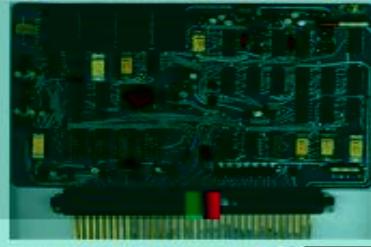


Power5 uP 2005 IBM



First Integrated  
Circuit By  
J. S. Kilby

Planar  
Process(1960)

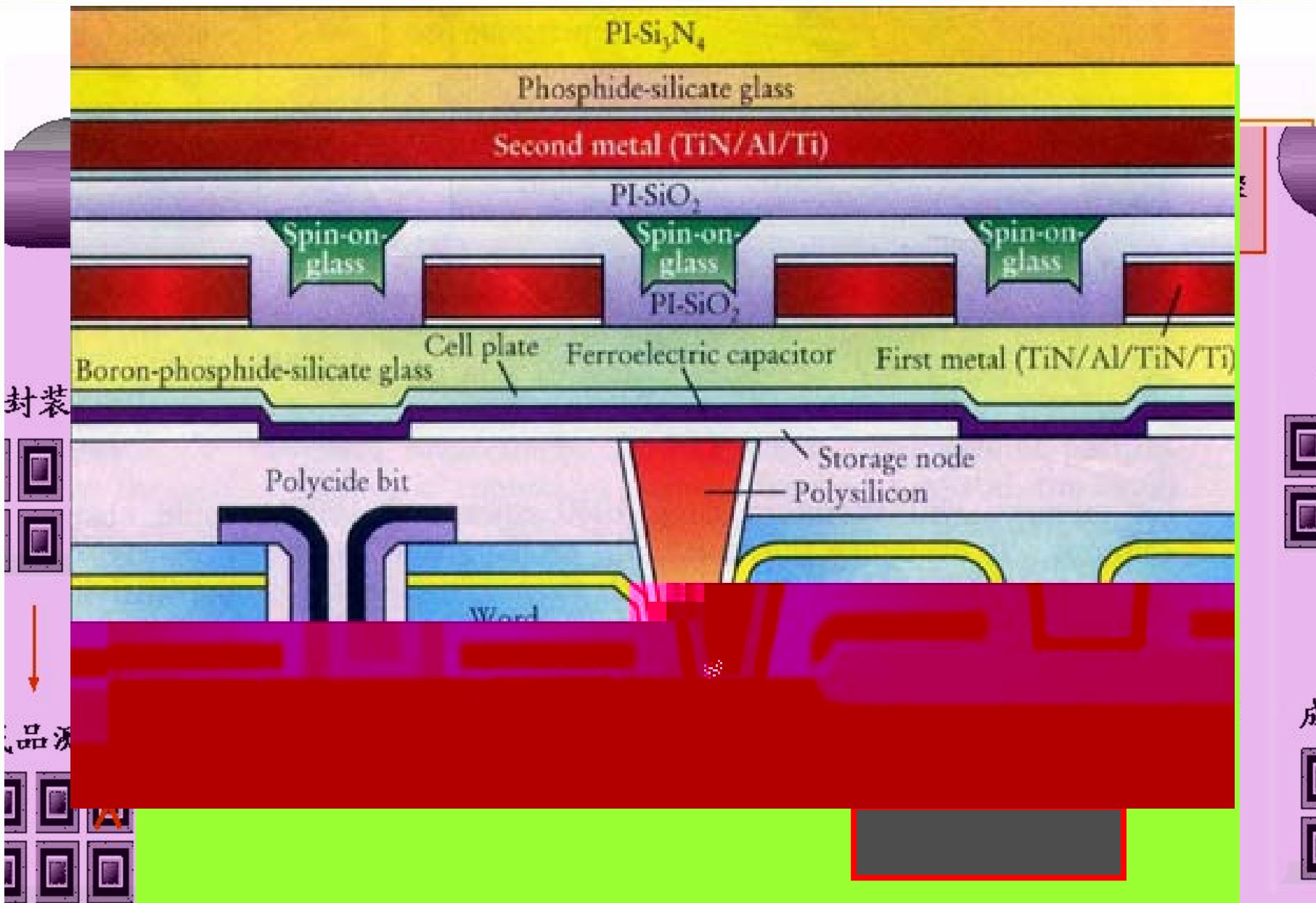


4004 uP 1971

1948

1958

18





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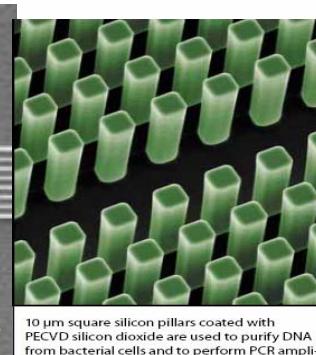
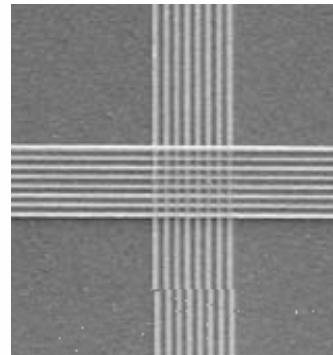
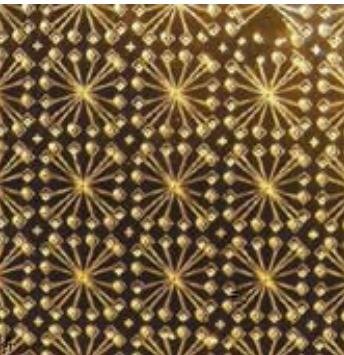
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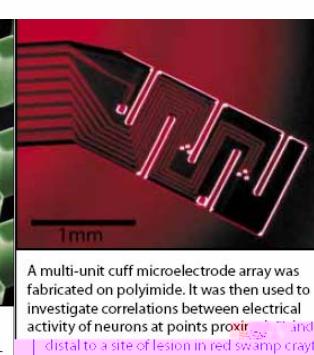
中国科学院微电子研究所  
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”

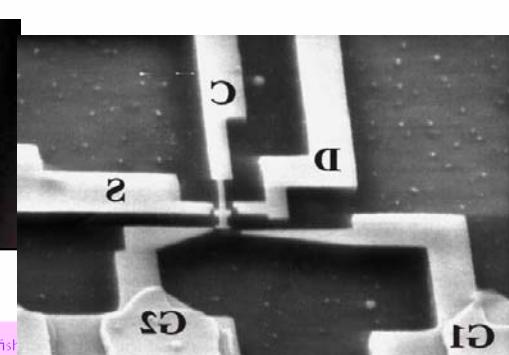
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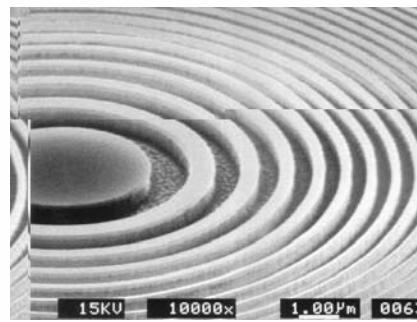
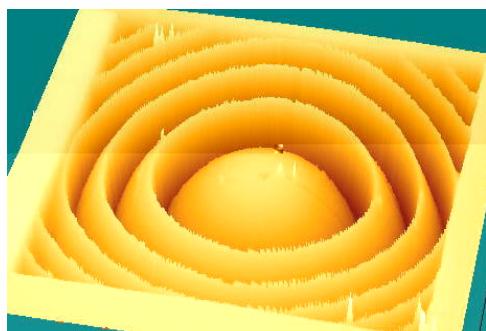
10  $\mu\text{m}$  square silicon pillars coated with PECVD silicon dioxide are used to purify DNA from bacterial cells and to perform PCR amplification via a miniaturized thermo cycler.



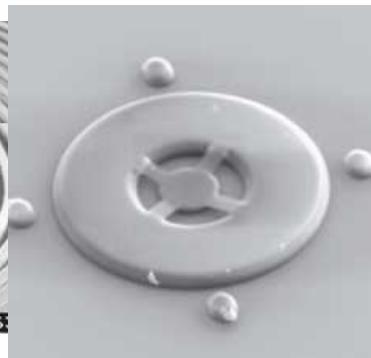
A multi-unit cuff microelectrode array was fabricated on polyimide. It was then used to investigate correlations between electrical activity of neurons at points proximal and distal to a site of lesion in red swamp crayfish Procambarus Clarkii.



SET



bragg-Fresnel lens for x-rays



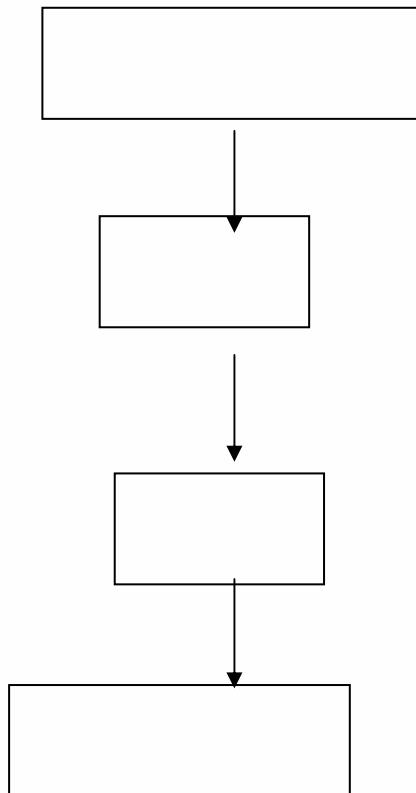
Nano fluidic  
accelerometer



3D



# Top-down

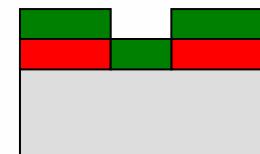
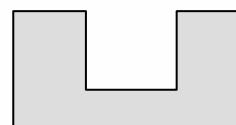
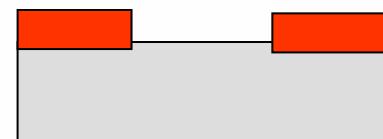


Si, GaAs, InP,  
Quartz, glass, metal

Photon sensitive polymer  
called “resist”

Optical, e-beam, ion beam  
x-ray, proton, STM/AFM

Etching, lift-off, imprint





# Top-down



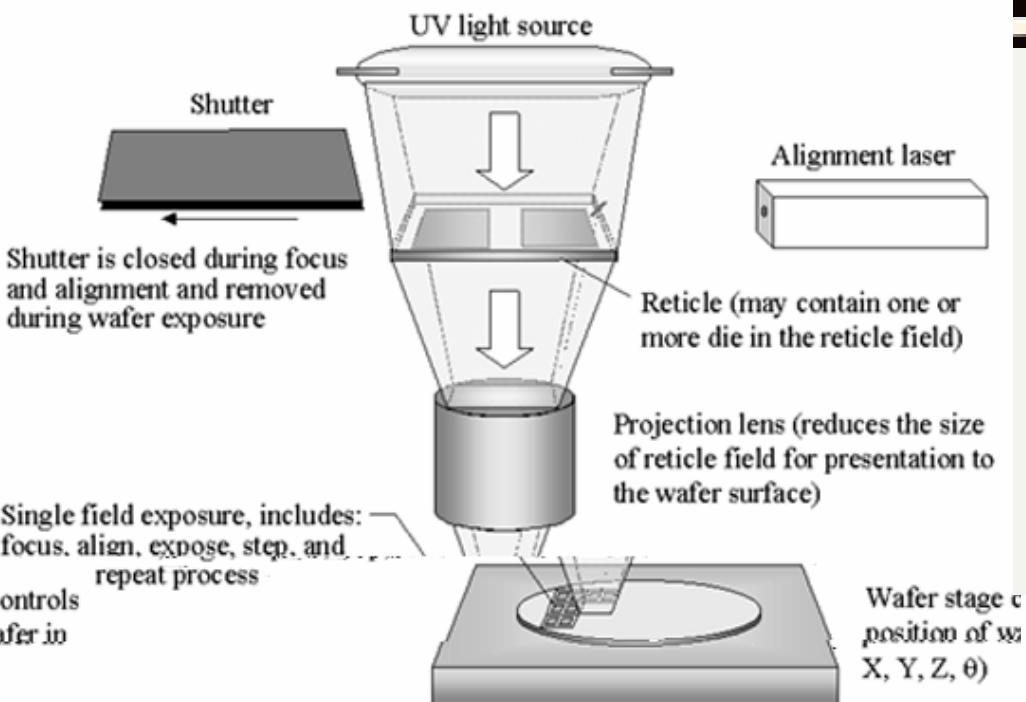
# Top-down

- Optical lithography
- E-beam lithography
- EUV (extra-ultra violet) lithography
- Nano-imprint



I TRS 2009

DRAM



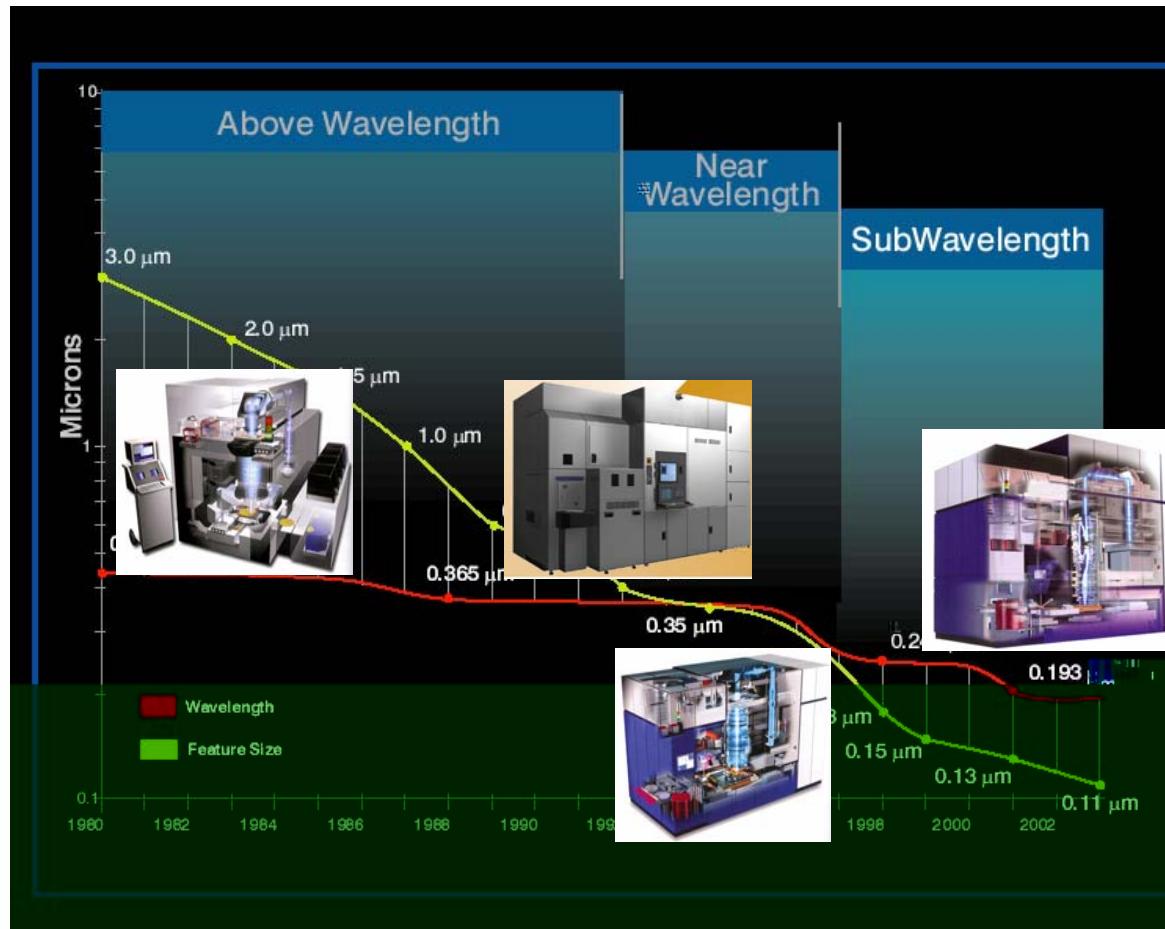
stage

wafer      IC

40-50% total wafer process time, Determine the minimum feature size



1978  
ASML  
1.5  
g GCA



30

ASML Nikon Canon

10

45nm



546 (e-line), 436(g-line), 365(i-line),  
248(KrF), 193(ArF), 157(F<sub>2</sub>), EUV  
(0.1-0.05Å), , X

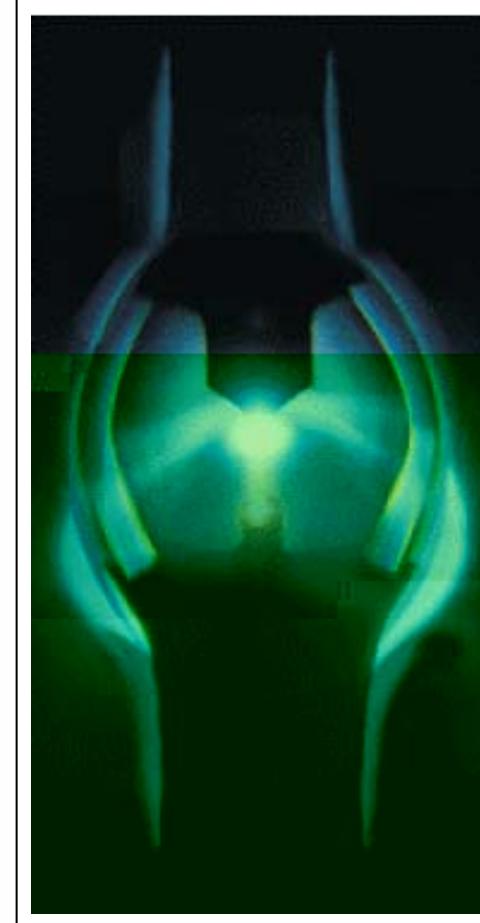
0.2—0.85—1.44( )





# 1 Using light source with shorter $\lambda$

	$\lambda$ (nm)		
	436	g	$>0.5\mu\text{m}$
	365	i	$0.5/0.35\mu\text{m}$
KrF( )	248	DUV	$0.25/0.13\mu\text{m}$
ArF( )	193	193DUV	$90/65\dots 32\text{nm}$
Xe	13.5	EUV	Reflective mirrors

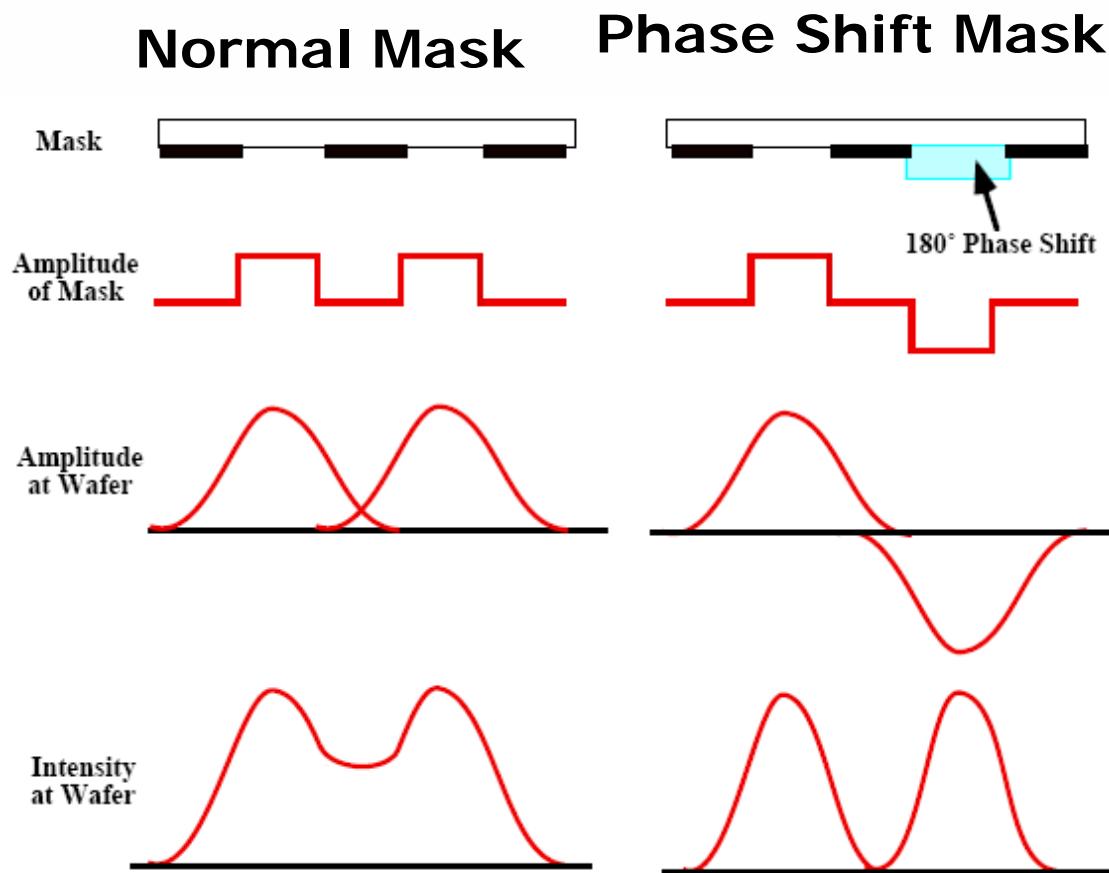




## 2 reduce k<sub>1</sub>

$$I = E \times E^*$$

Pattern dependent  
 $k_1$  can be reduced by up  
to 40 %





# Optical Proximity Effect Correction

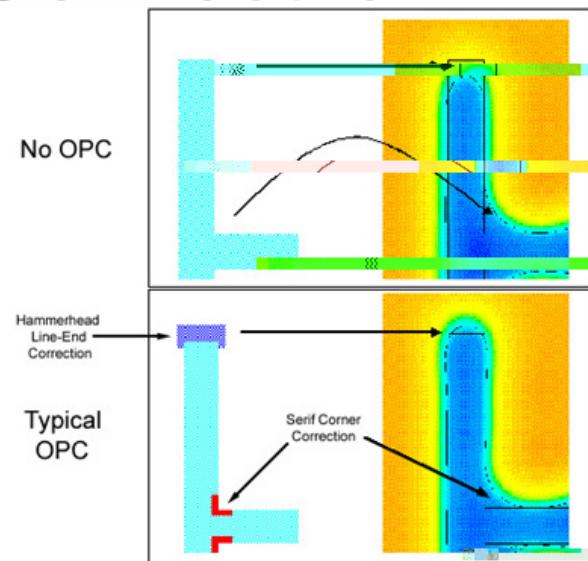
OPC

diffusion  
effect  
OPC

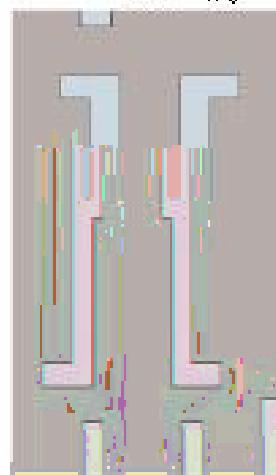
OPC

OPC

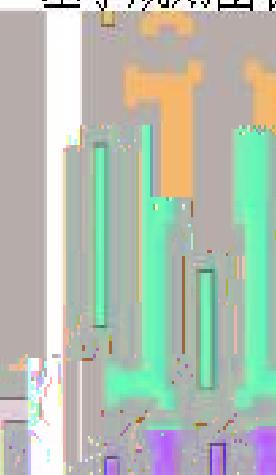
loading



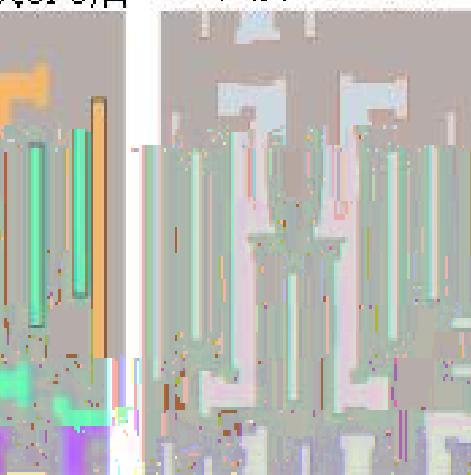
OPC 前



基于规则图表OPC后



基于模型OPC后





# 3

## Lens fabrication

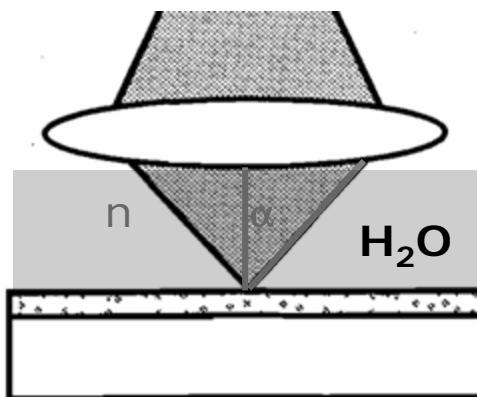
$\lambda$ [nm]	NA
436	0.15-0.45
365	0.35-0.60
248	0.35-0.82
193	0.60-0.93

$$R = k_1 \frac{\lambda}{NA}$$

$$n_{H_2O} = 1.44 \Rightarrow NA \approx 1.36$$

State of the Art:  $\lambda=193$  nm,  $k_1=0.3$ ,  $NA=0.93 \Rightarrow R \approx 60$  nm  
 $= 1.36 \Rightarrow R \approx 40$  nm

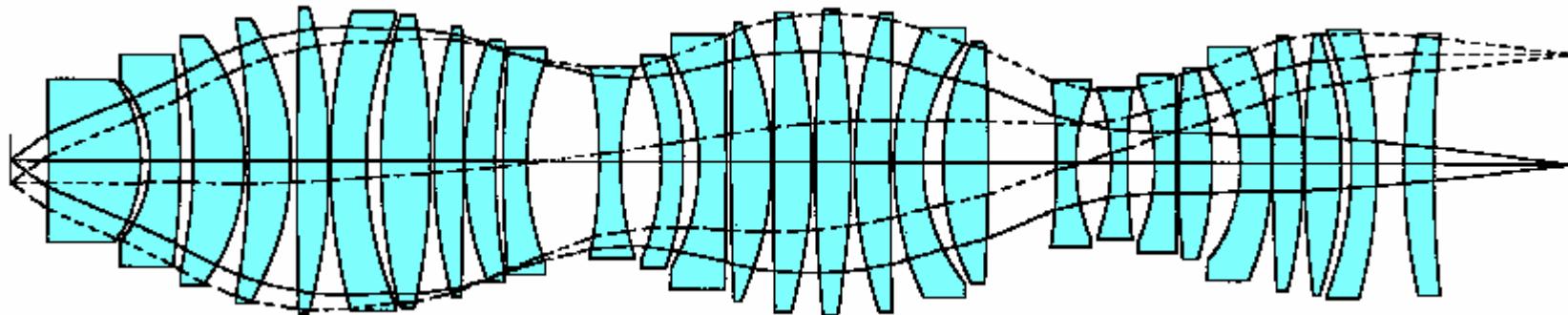
## Immersion Lithography



Numerical Aperture:  
 $NA = n \sin \alpha$



# KrF--Stepper Projection Lens



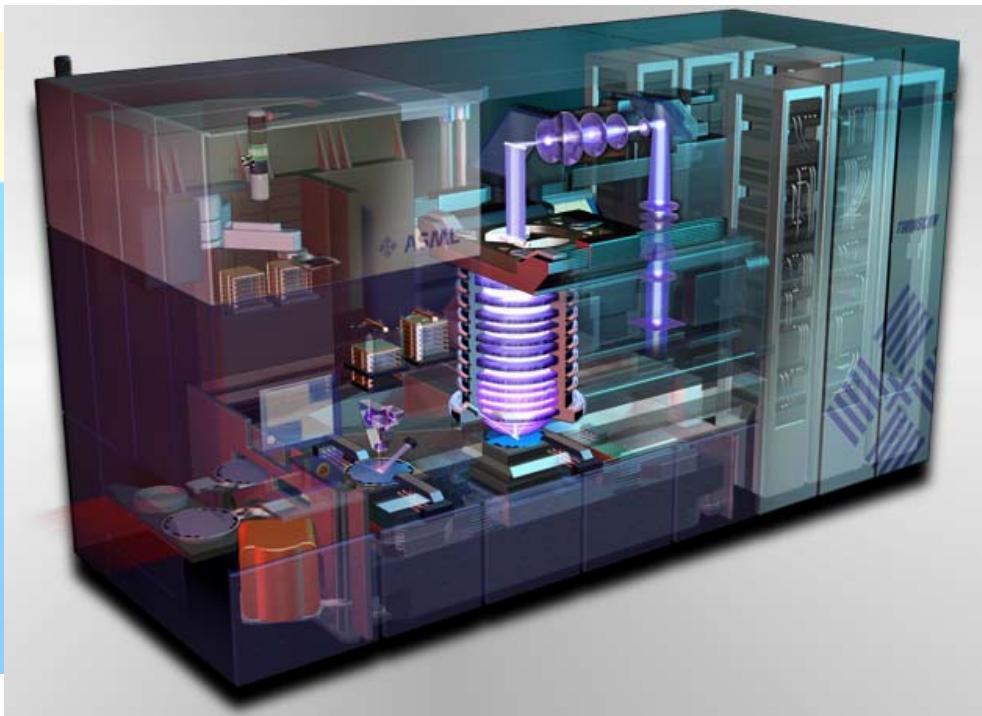
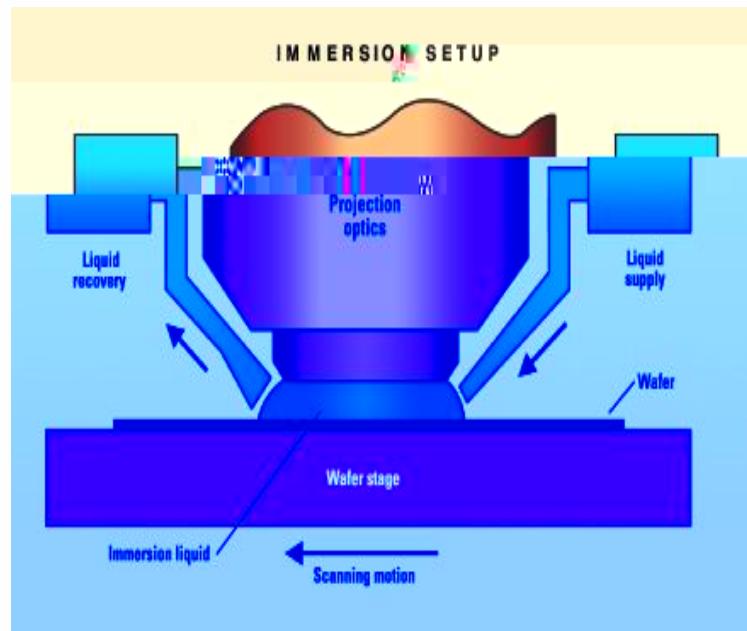
$\lambda = 248\text{nm}$  NA=0.55 Filed: 22mm, 28-lens optics

Japan Patent: Laid Open 8-166540

$\lambda = 193\text{nm}$  NA=0.75 Filed: 22mmx26m, 38-lens ,  
a spherical optics



# 193nm



n

1.44

32nm

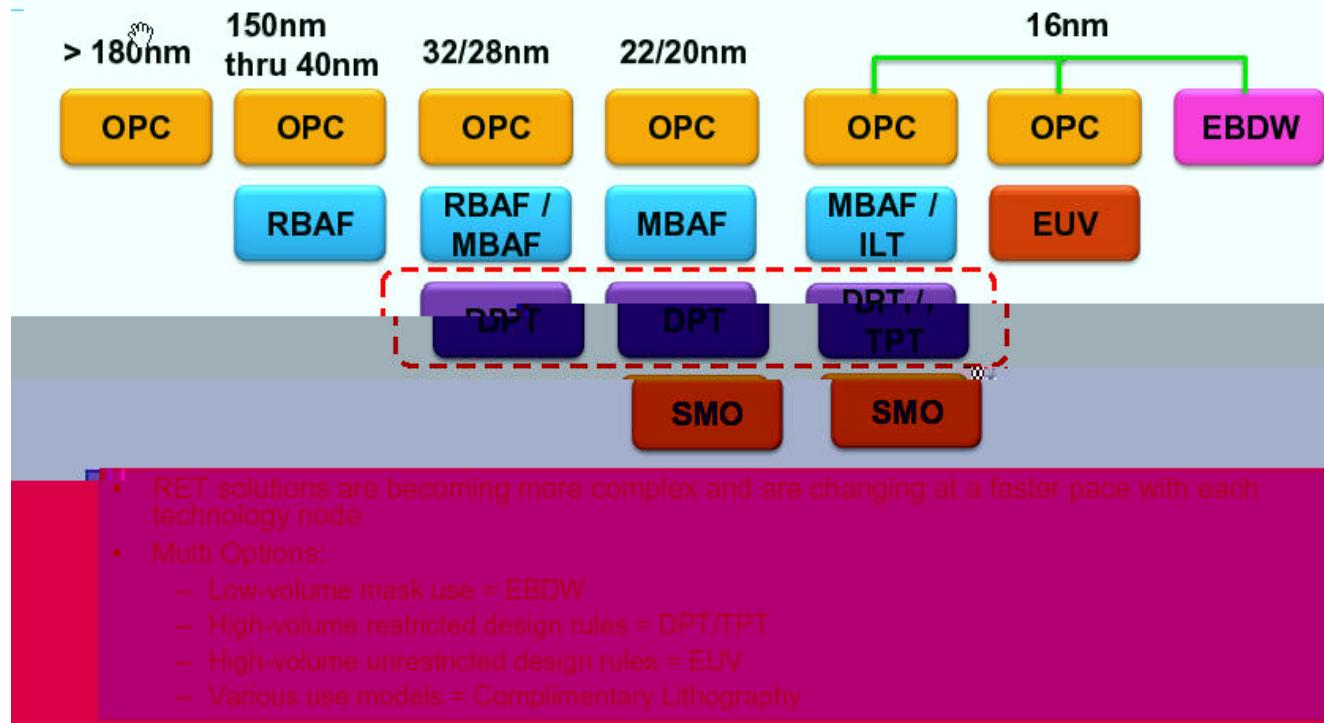
193nm



- OPC RET
- 
- 
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-



## Technology RET Solutions



IC

RET

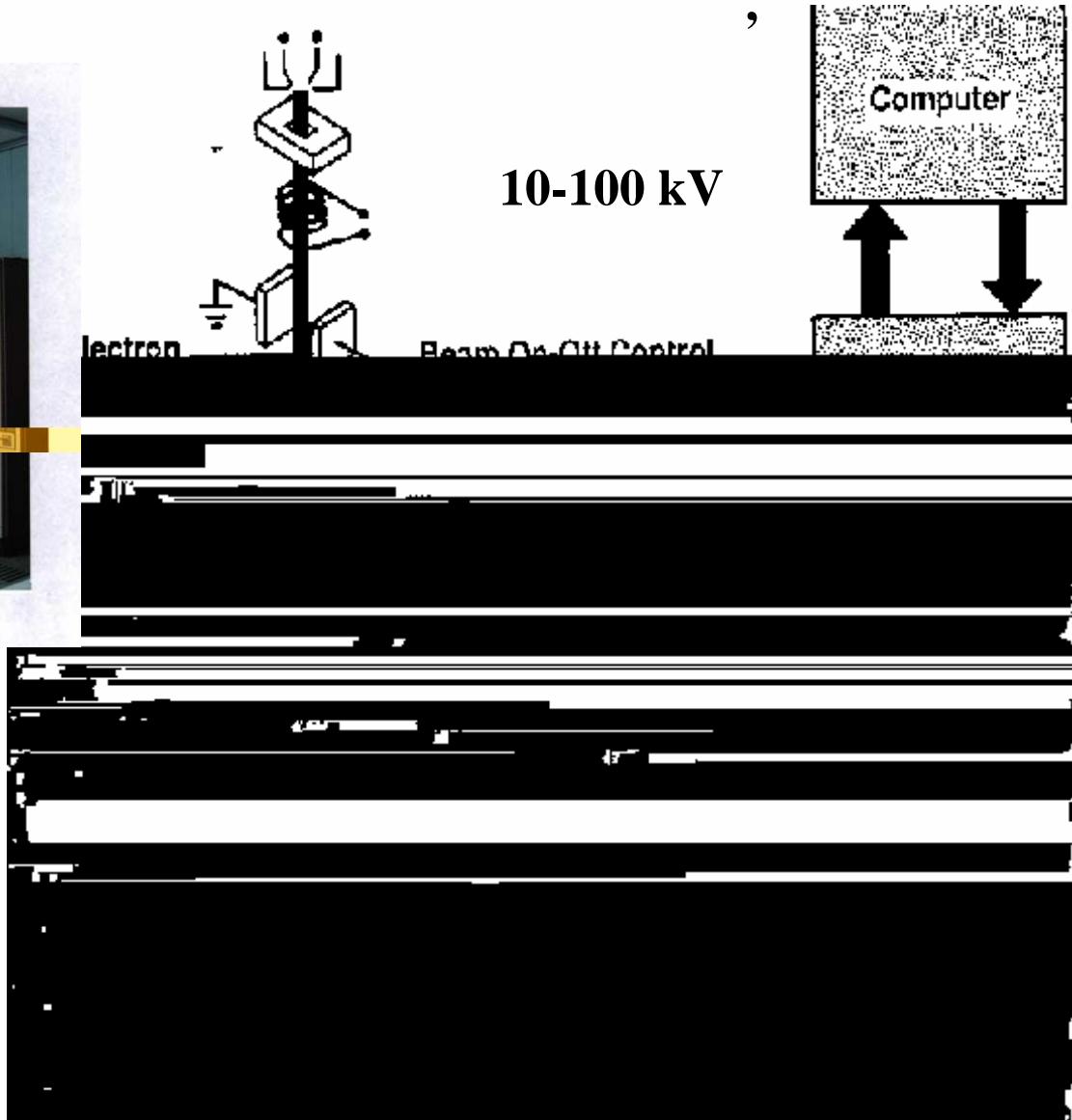


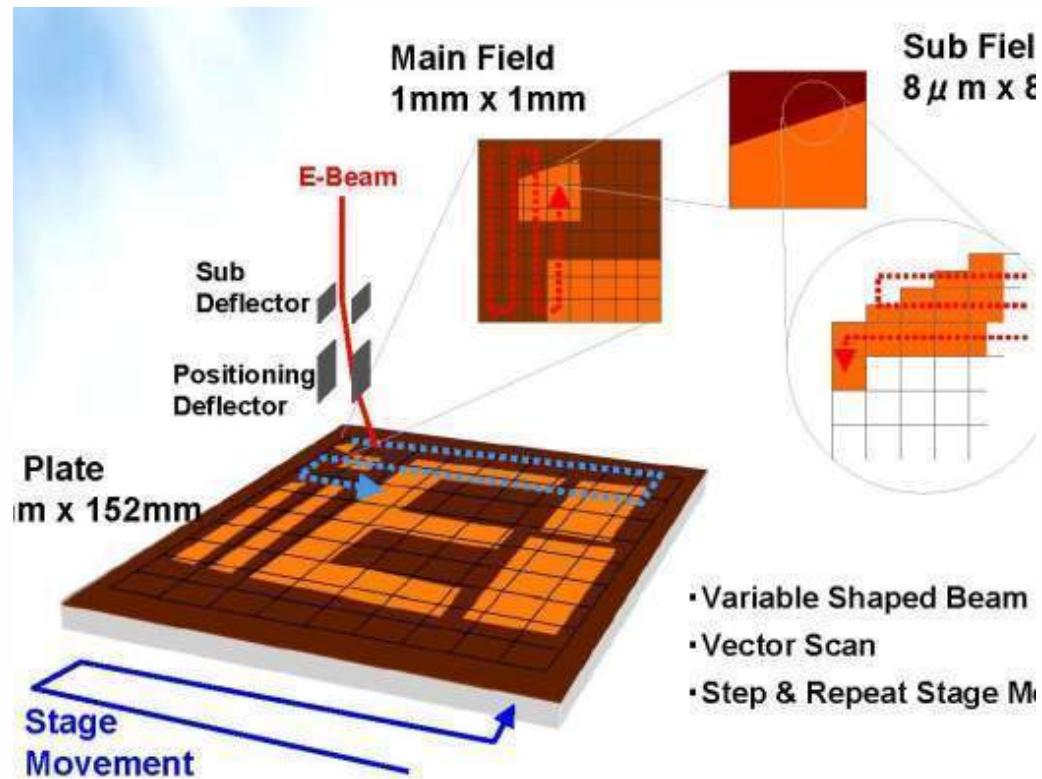
# Top-down

- Optical lithography
- E-beam lithography
- EUV(extra-ultra violet) lithography
- Nanoimprint



Stage

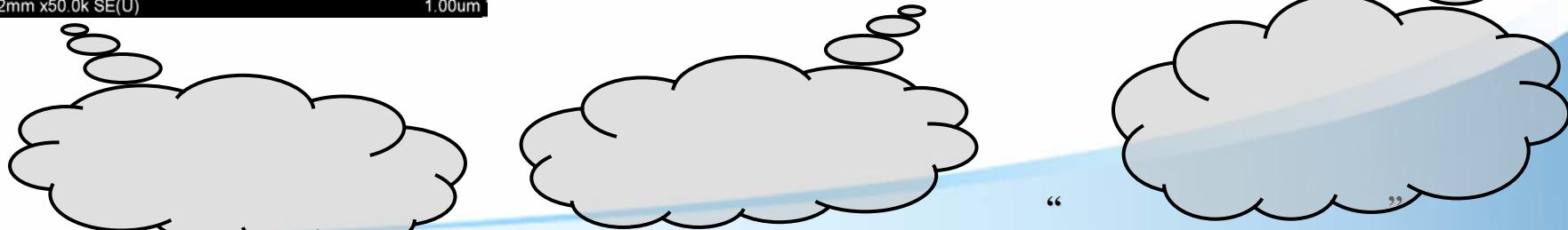
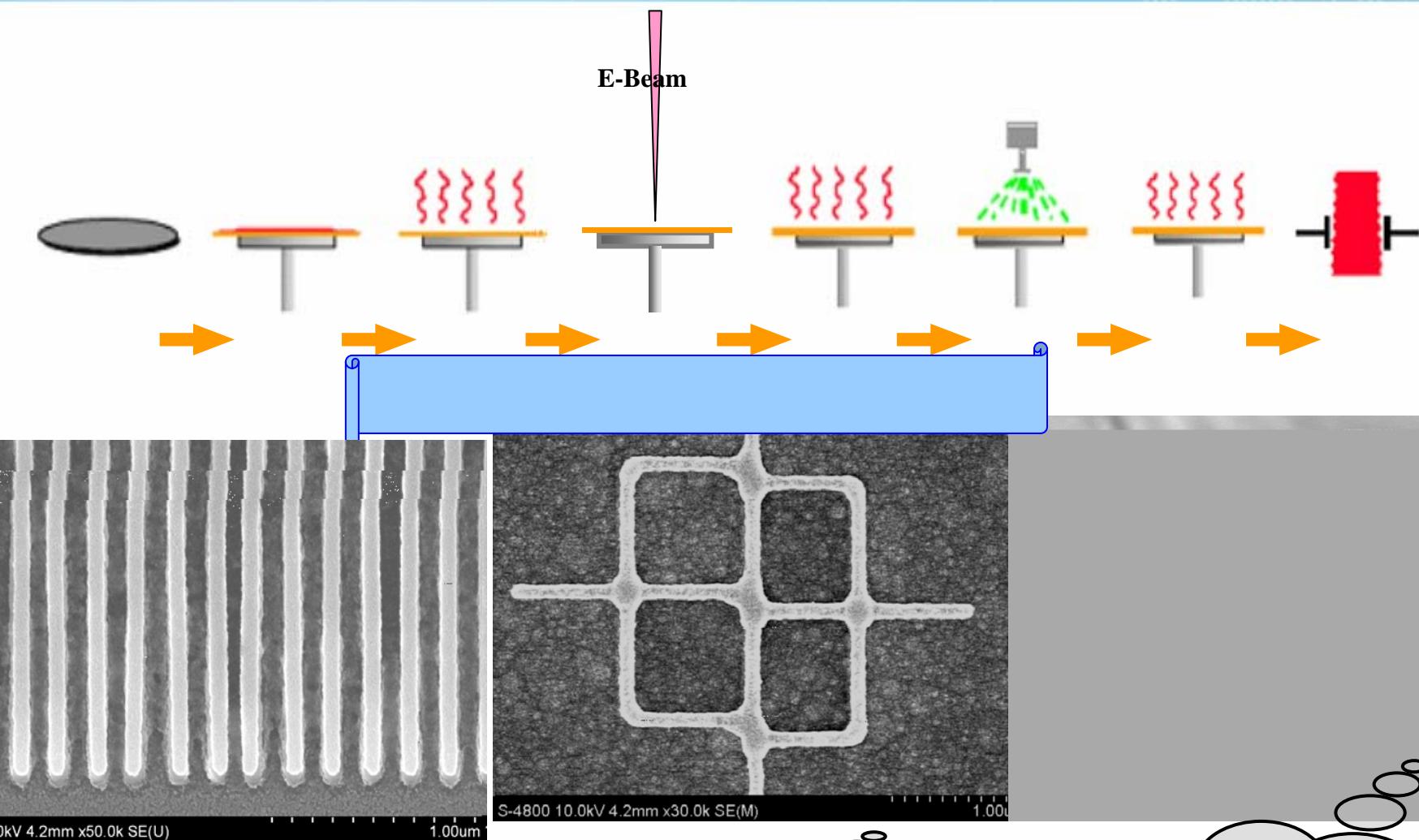




Similar principle as a scanning electron microscope  
Electron-beam process Mix and Match Technology Resist technology  
Simulation Proximity effect correction



- ( $< 5\text{nm}$ )
  - Large substrate size (12" wafer)
  - Multi-level patterning (alignment error  $< 20\text{nm}$ )
  - High flexibility
  - Fast prototyping
- 
- **spot size**-electron lens aberration, space charge, mechanical stability
  - **proximity effect**-electron forward and backscattering
  - **resist contrast**





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50

0.125-12.5nm

2-50nm

5-100nm

1-50nm

1-50nm

20-1000nm

20-500nm

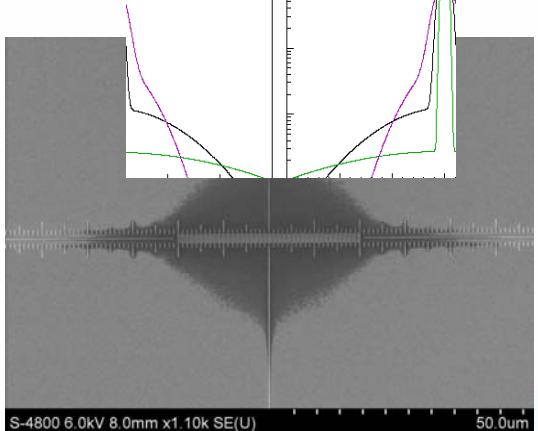
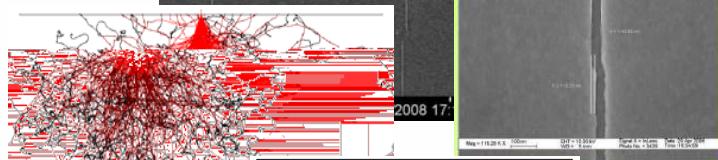
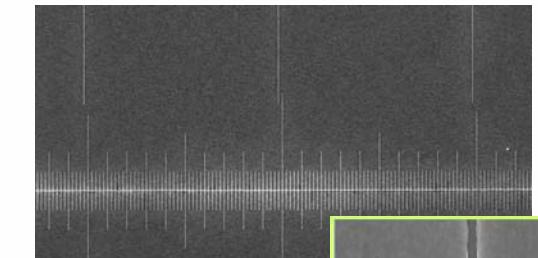
15-60nm

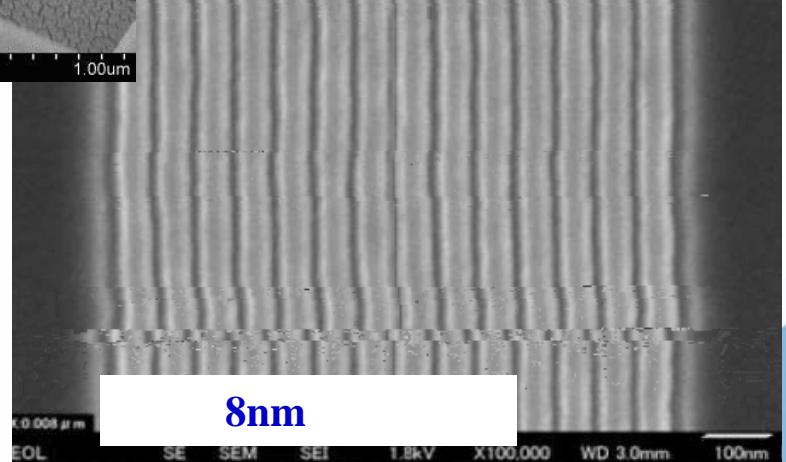
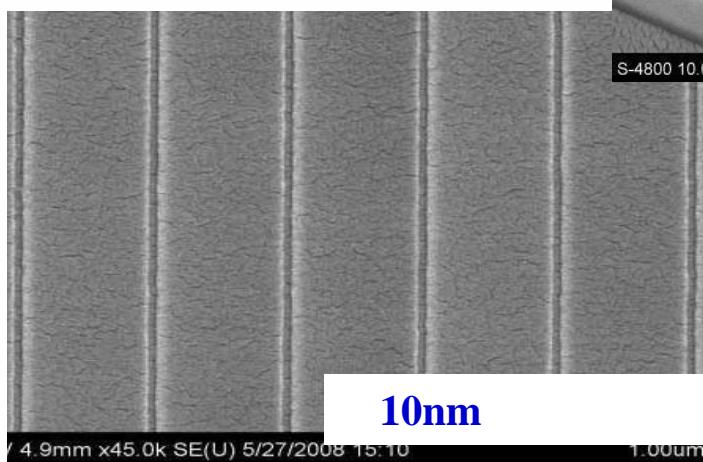
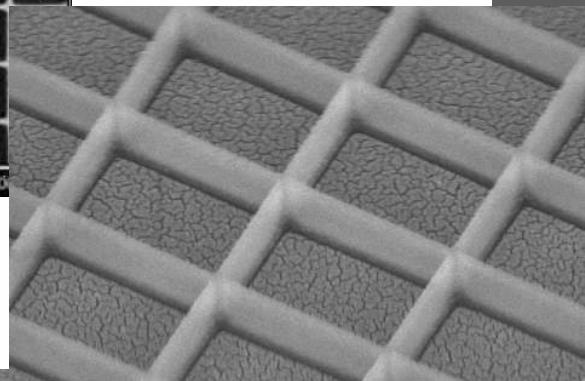
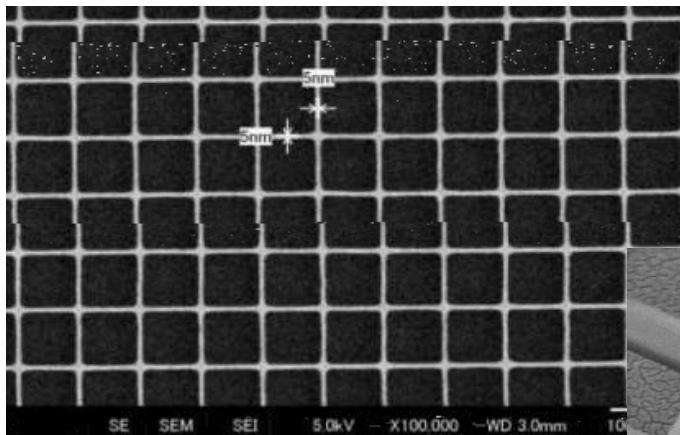
2-50

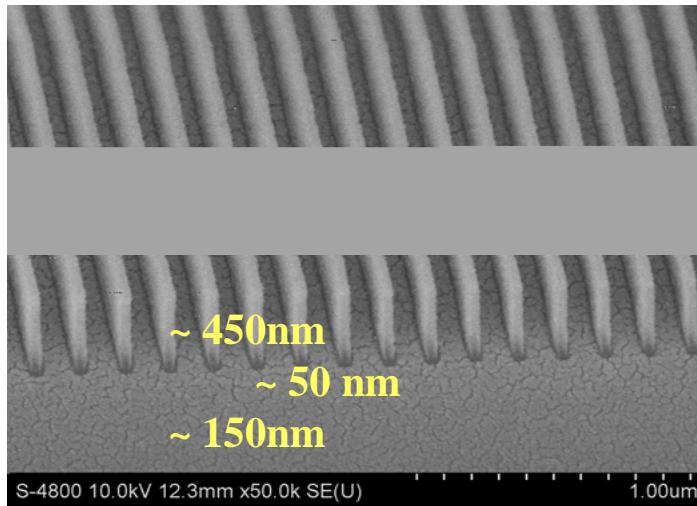
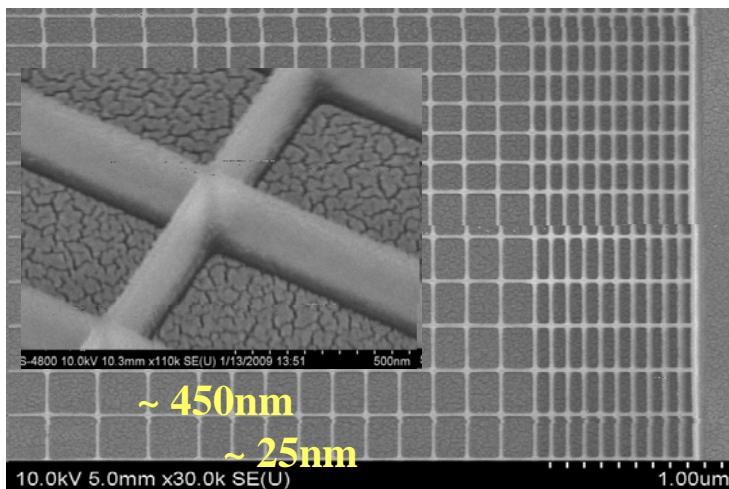
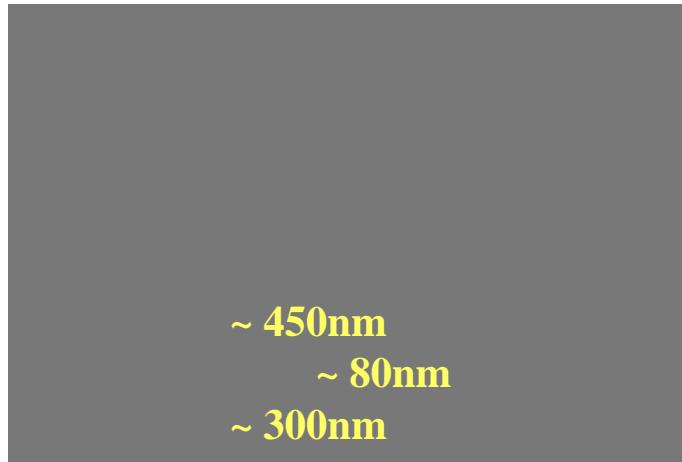
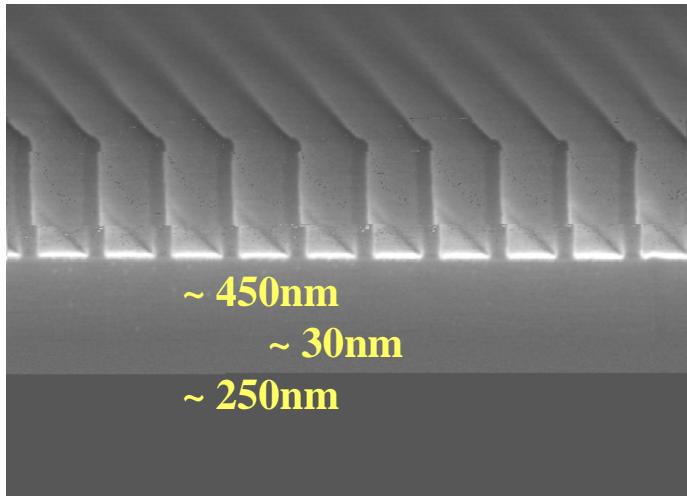
10-100nm

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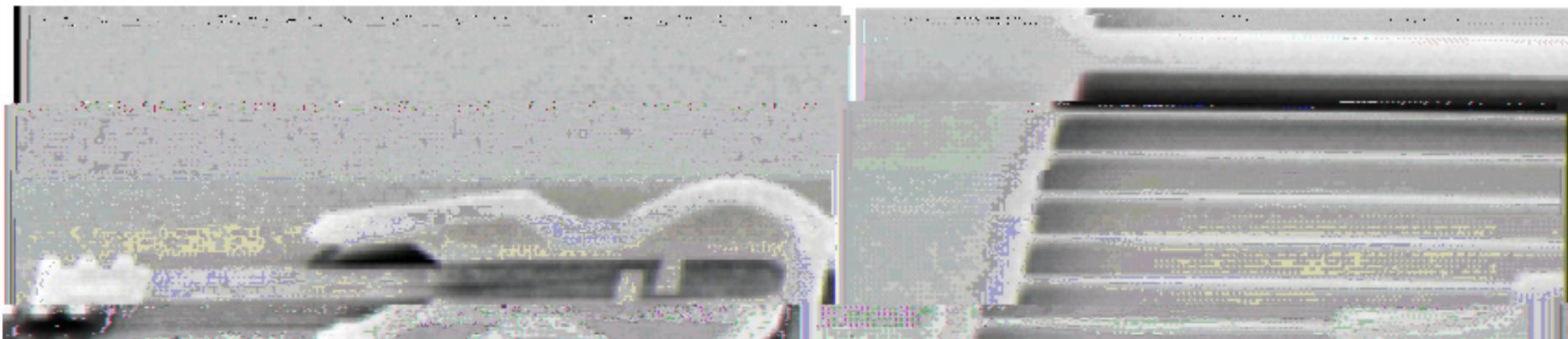








## Nano-Scale Fabrication



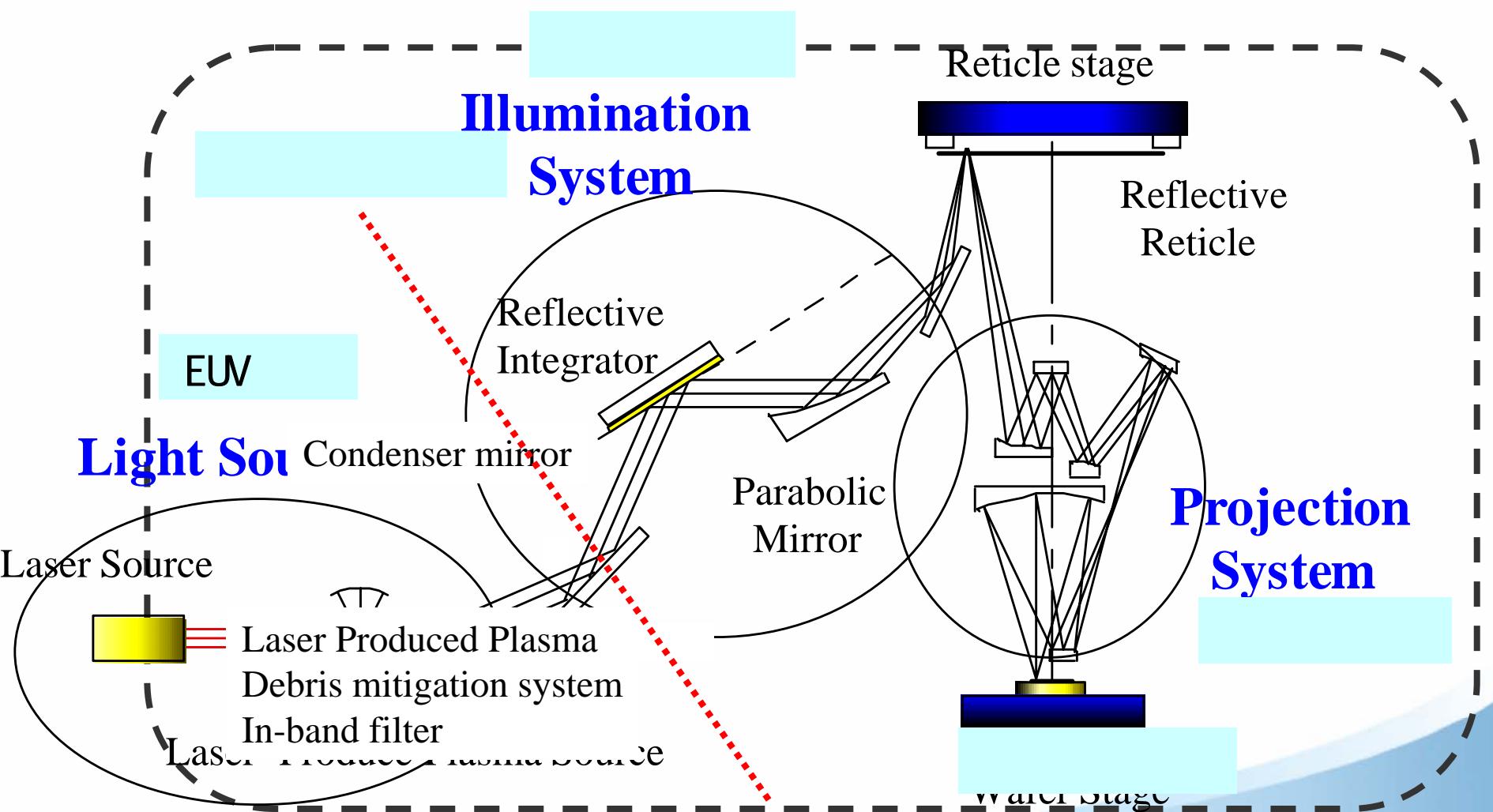


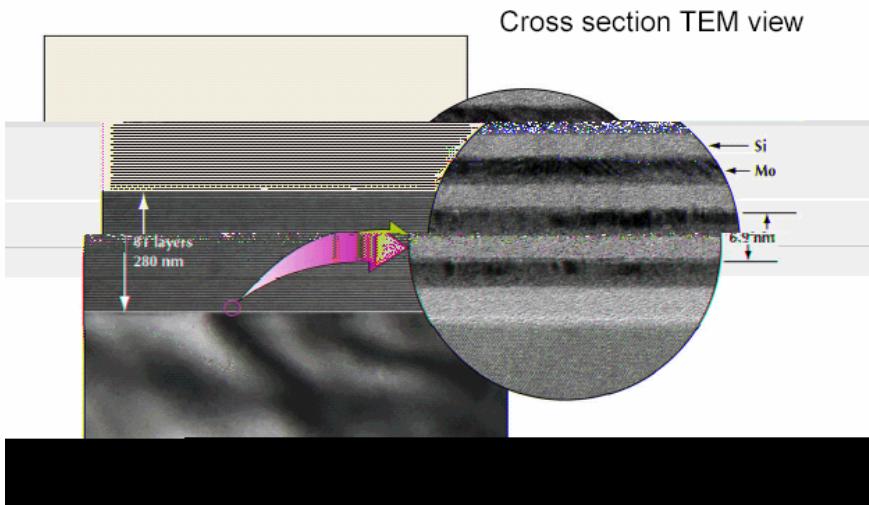
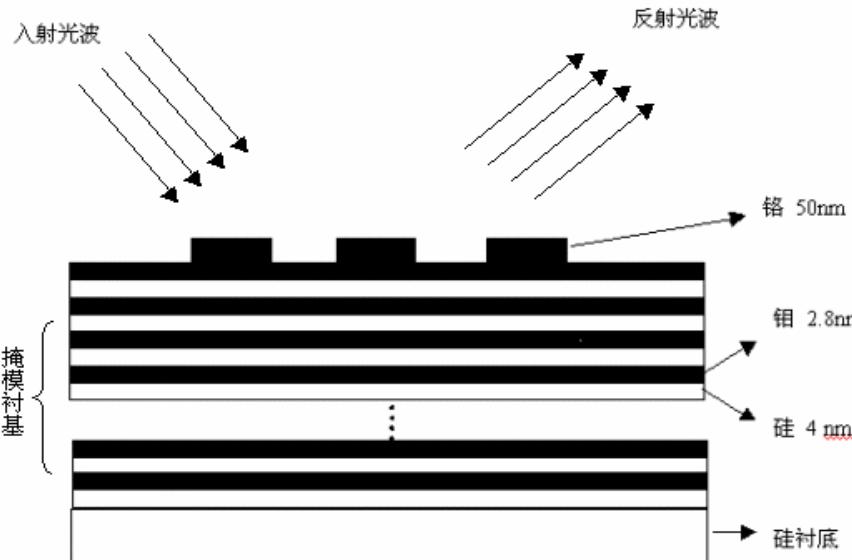
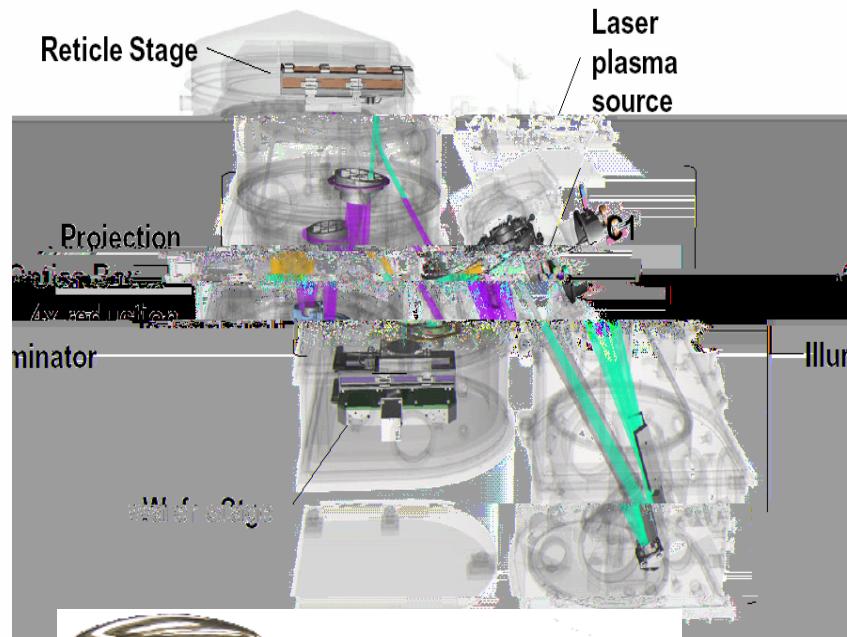
# Top-down

- Optical lithography
- E-beam lithography
- EUV(extra-ultra violet) lithography
- Nano-imprint



EML







# EUVL



2006 8 , ASML      IMEC CNSE(University at Albany)  
SCANNER, More Moore EUV      α EUVL  
22nm      5000  
EUV



# EUVL

(A): KrF resist  
high resolution  
reasonable TP

(EUVL)

- 
- 
- 
- 
- 



**80wph    300mm    32nm**

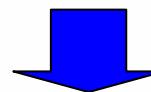
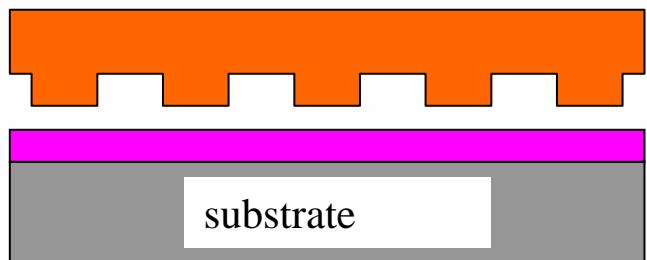


# Top-down

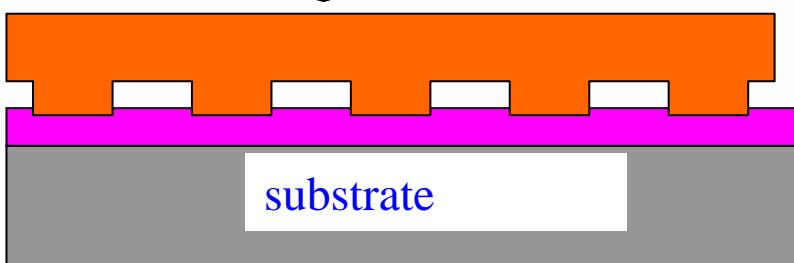
- Optical lithography
- E-beam lithography
- EUV(extra-ultra violet) lithography
- Nano-imprint



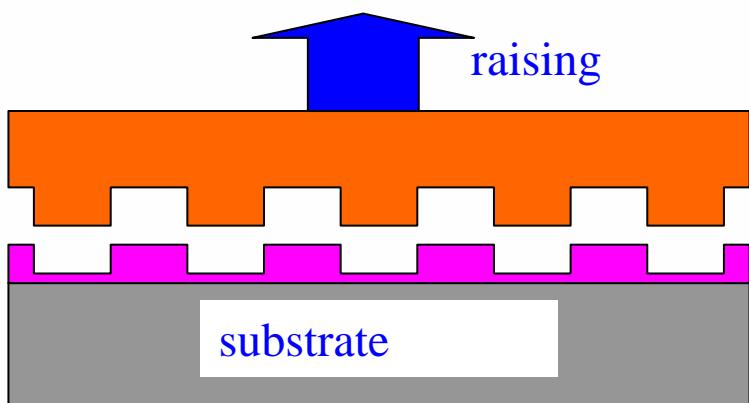
mould  
resist



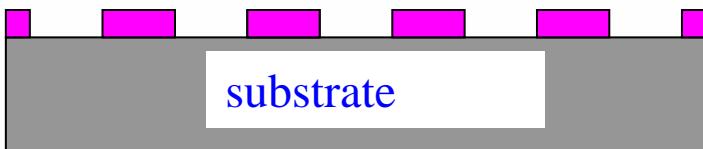
pressing



raising



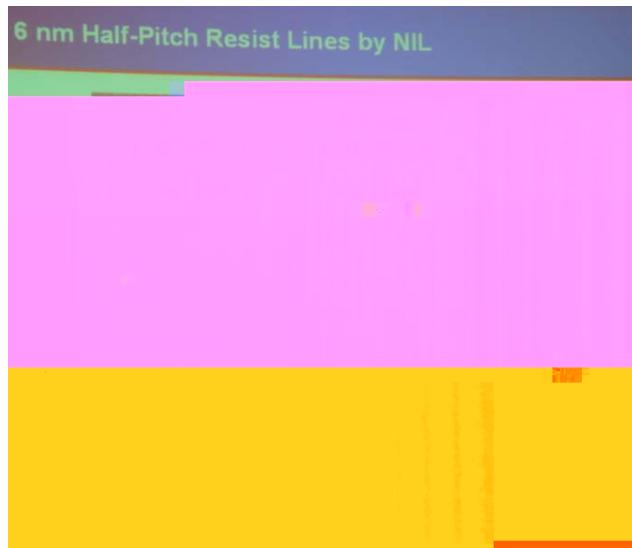
RIE to remove residual





## (1) Comparison of EBL & NIL:

	EBL	NIL
1. Resolution:	+	+
2. Throughput:	-	+
3. Cost	-	+
4. Alignment	+	?
5. Damage	-	+



## (2) Applications

Nano CD,  
opto-devices (e.g. gratings),  
infrared detectors,  
Semiconductor devices

6nm N L

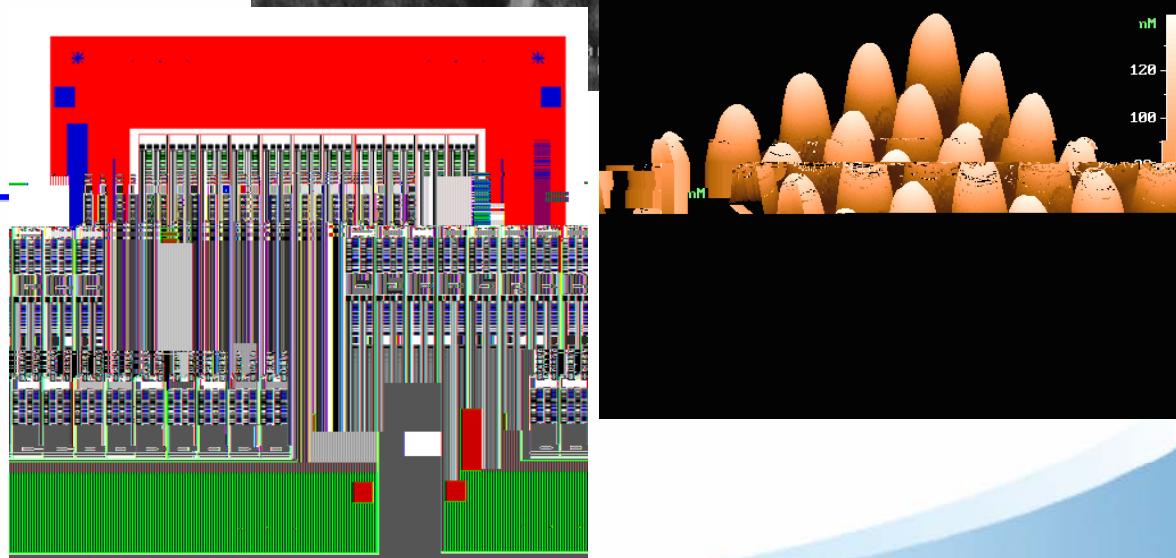
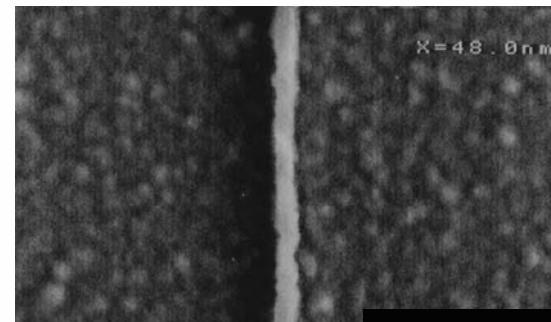
I EEE NAN007



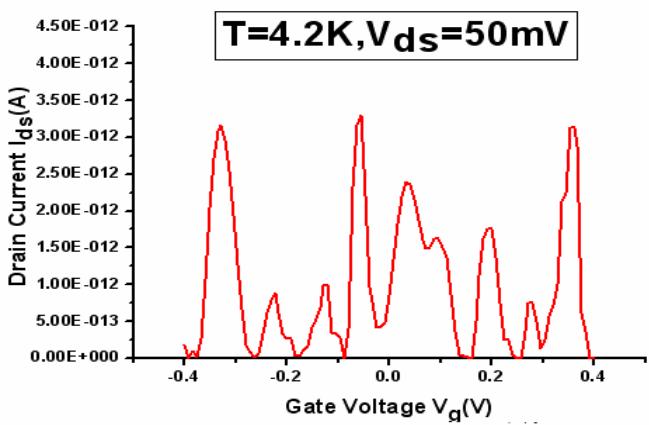
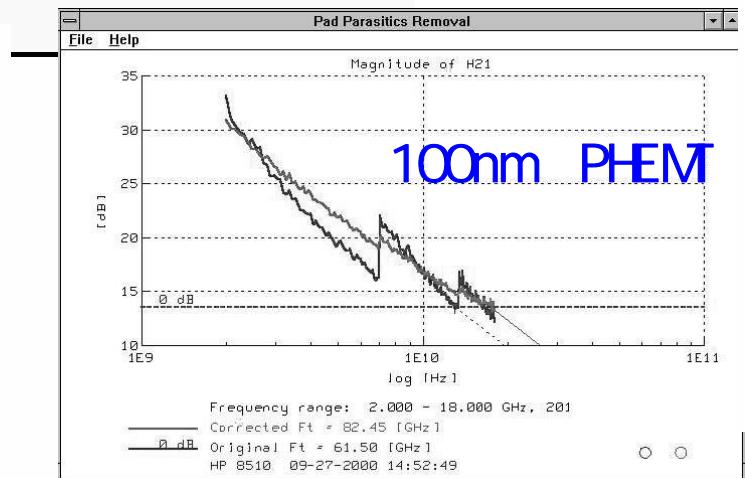
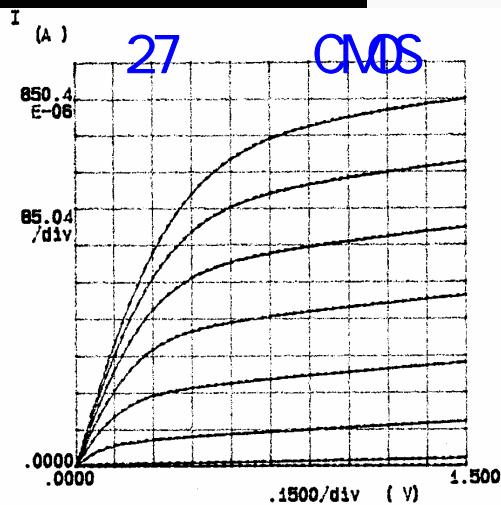
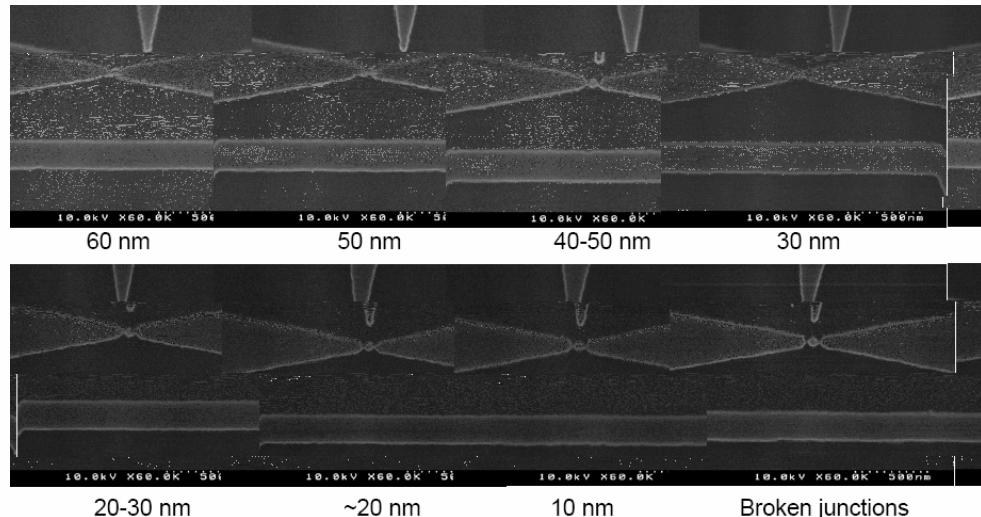
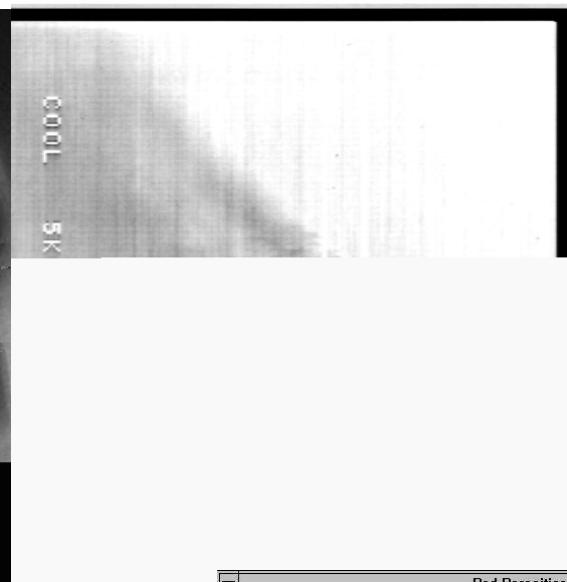
Top-down



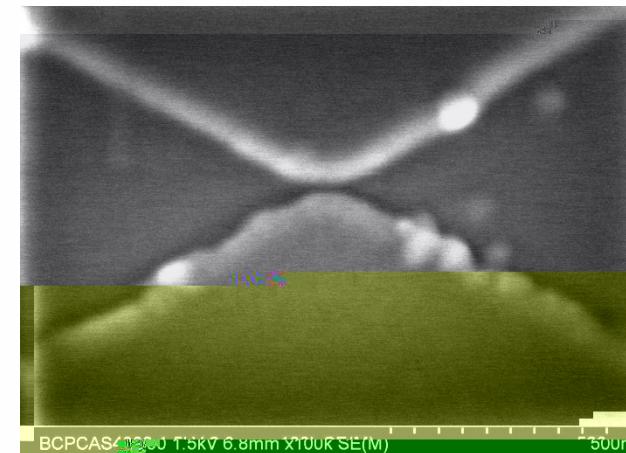
- ◆ Support more than 400 customers
- ◆ 12 national criterions on micro-lithography were issued from IMECAS



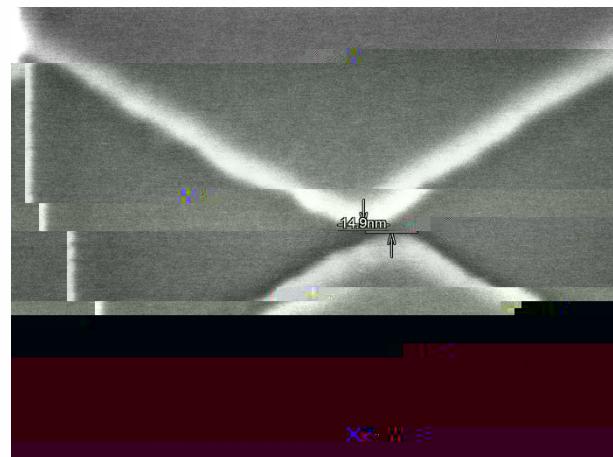
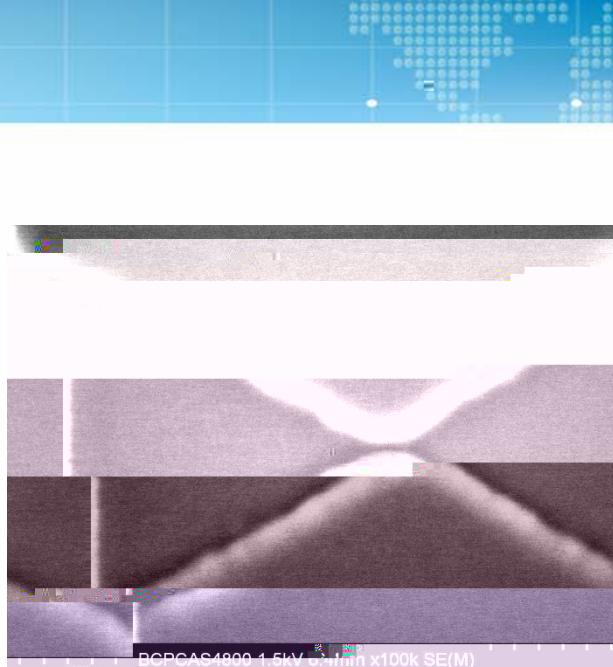
- ◆ Advanced optical mask and next generation mask-making;
- ◆ Nano-lithography: e-beam, x-ray, Nano-imprint;
- ◆ Resolution Enhance Technology : OPC, PSM, etc
- ◆ Join in national key project



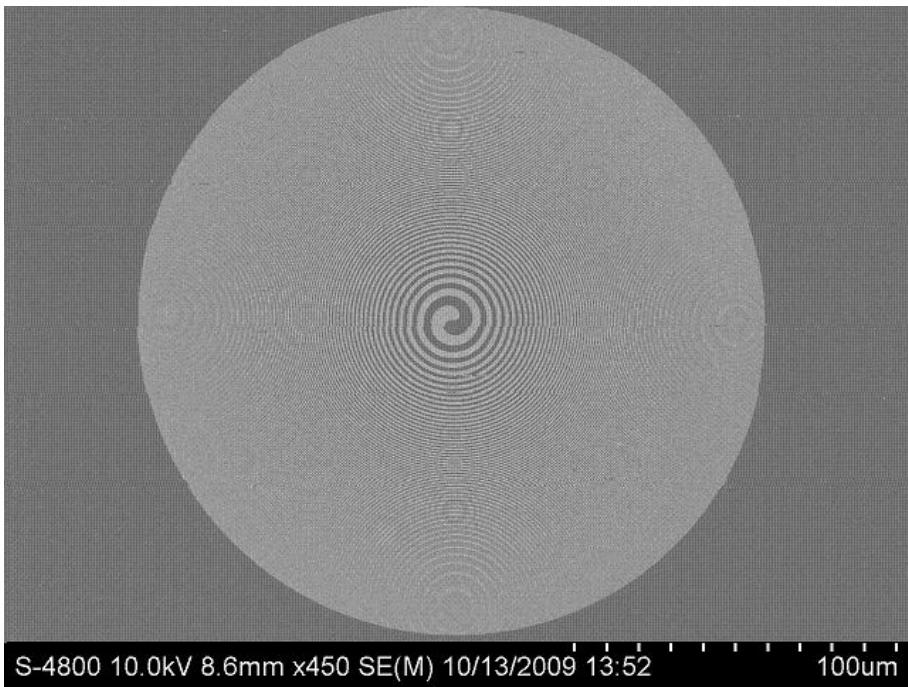
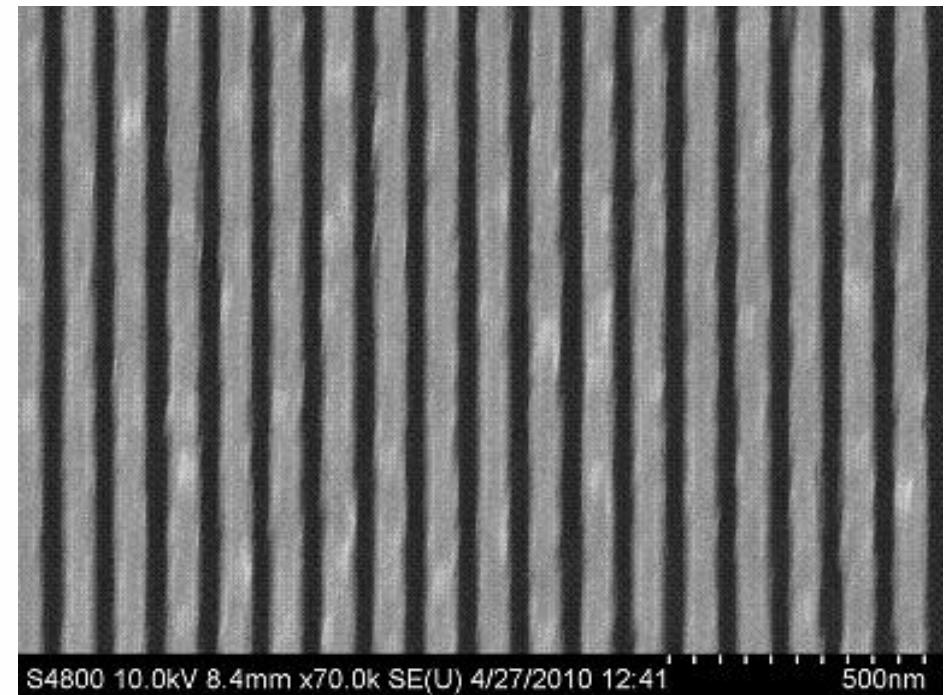
$$C_g = 1.1 \text{ aF}$$



13nm



100nm

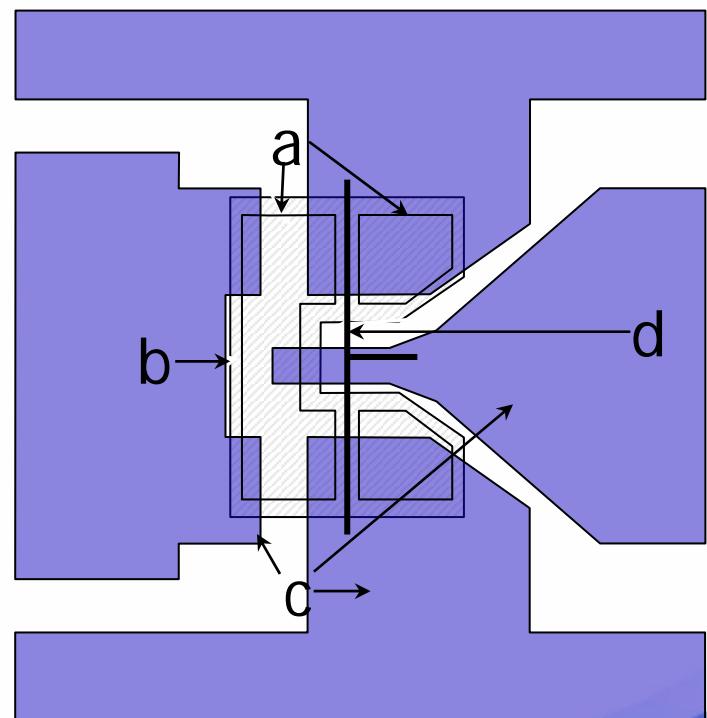
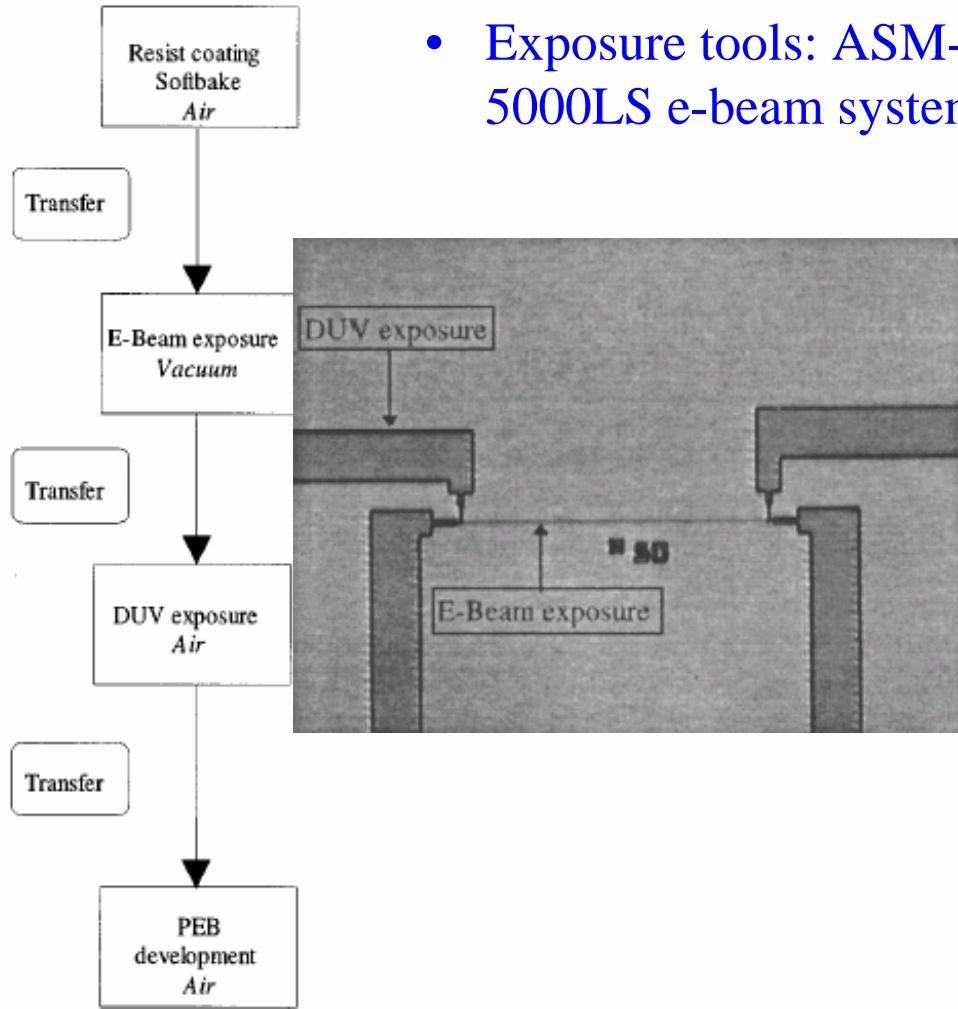


10000 /  
2mmx2mm

200

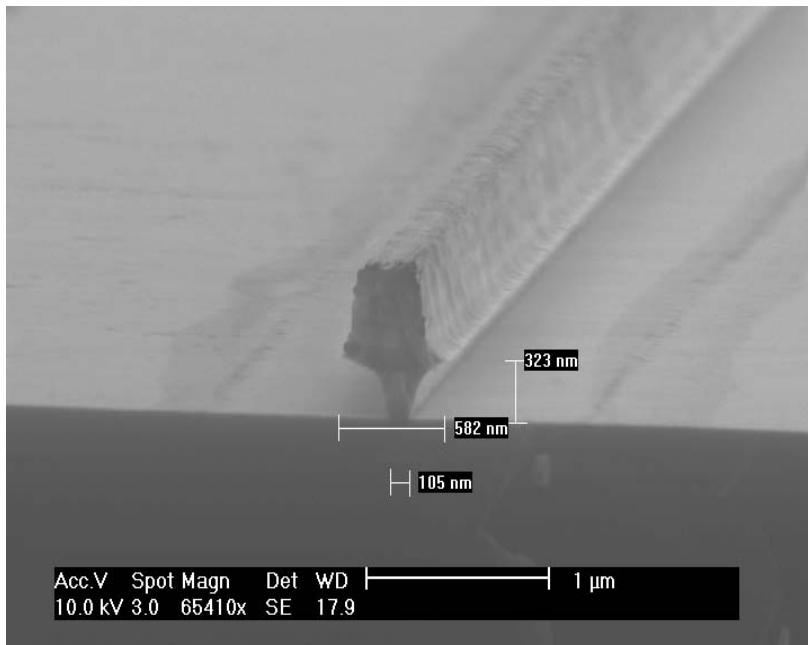


- Exposure tools: ASM-L 5000/55 stepper and JBX-5000LS e-beam system at 50keV

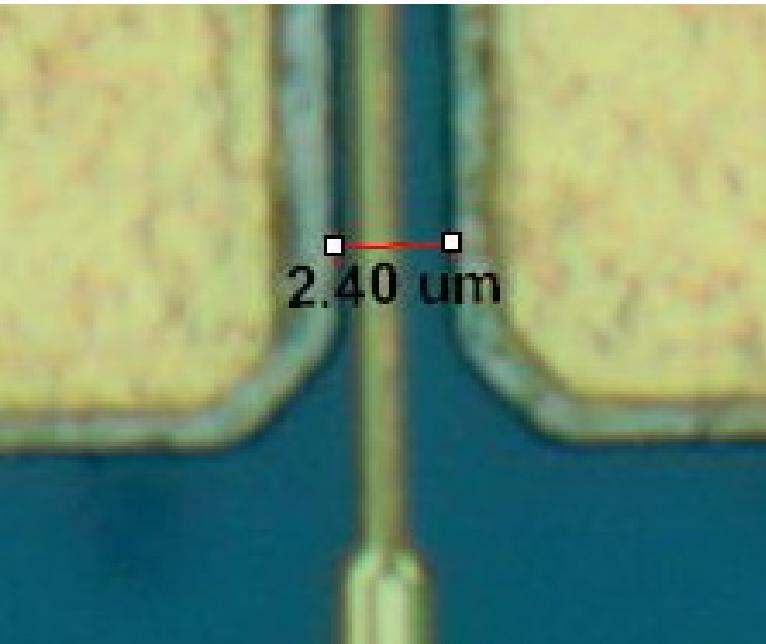




T

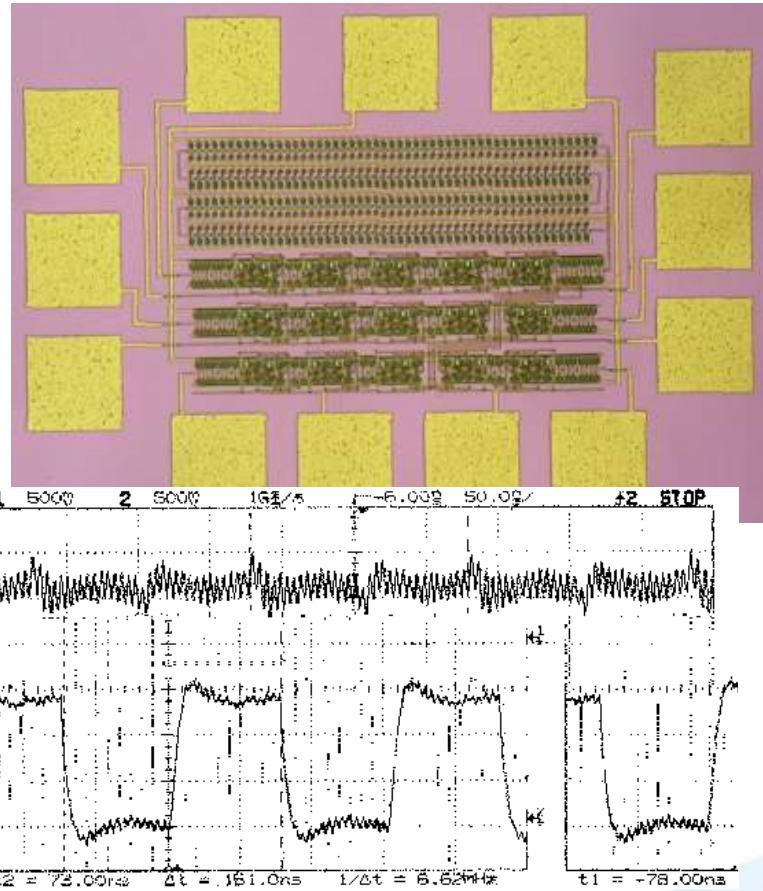
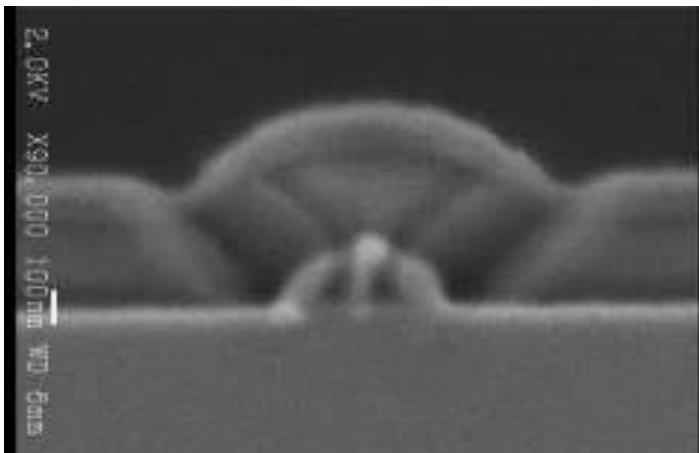
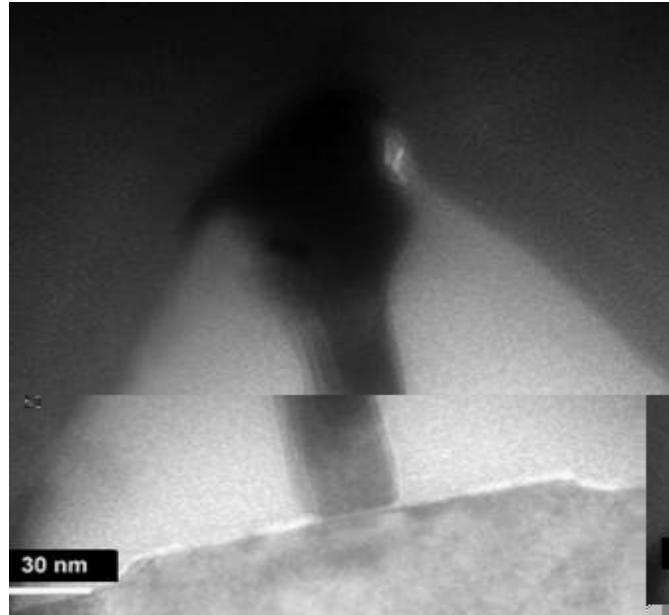


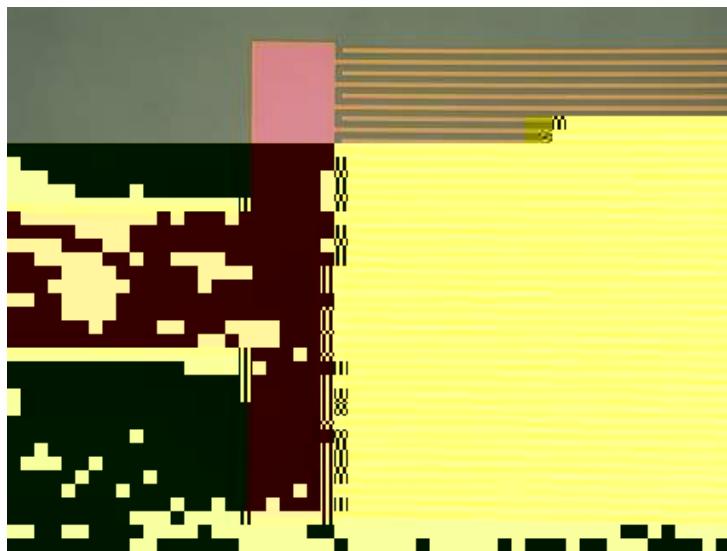
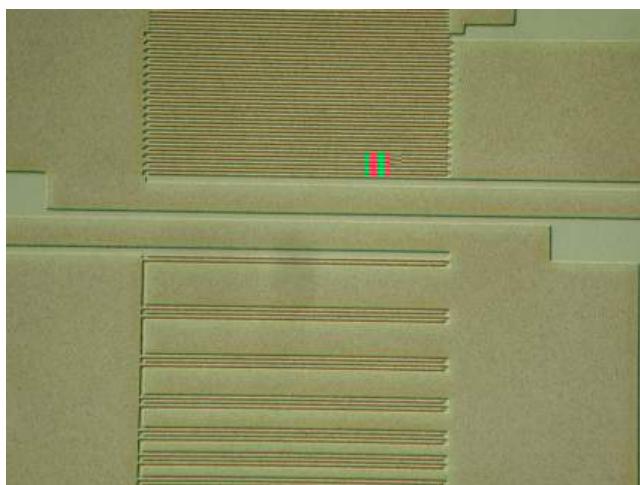
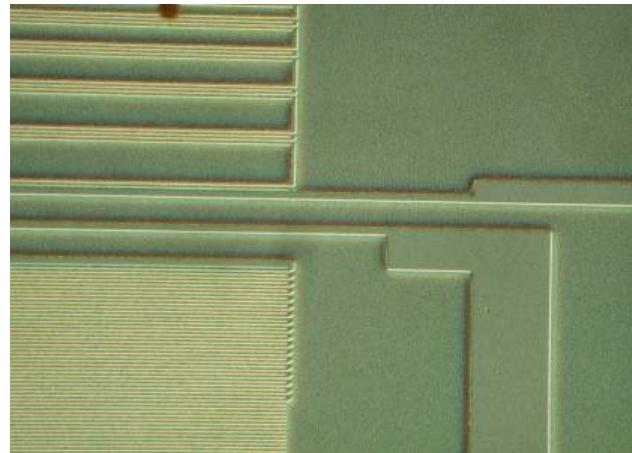
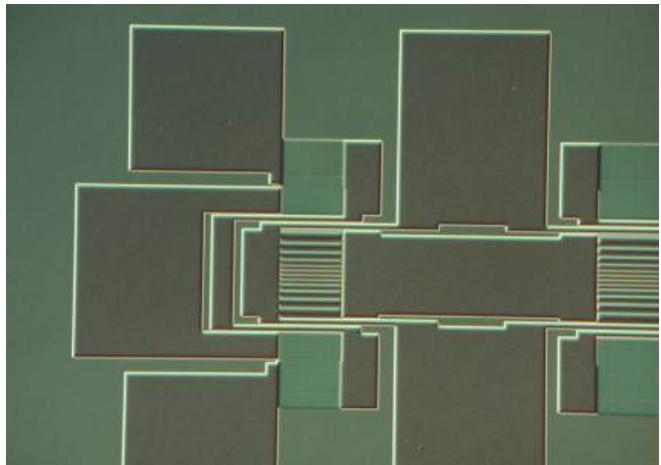
100nm T





CMOS







Top-down



中国科学院微电子研究所  
INSTITUTE OF MICROELECTRONICS OF CHINESE ACADEMY OF SCIENCES



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Q & A !